



Product Change Notification

106404 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmo.pcn@intel.com

Asia Pacific Contact: apacgccb@intel.com

Europe Email: eccb@intel.com

Japan Email: jccb.ijkk@intel.com

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Product Change Notification

Change Notification #: 106404 - 00
Change Title: Intel® Pentium® D 900 Sequence Processor,
PCN 106404-00, Product Design, C-1 to D-0
Stepping Conversion
Date of Publication: June 13, 2006

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date of Samples Availability:	Jul 17 , 2006
Date of Qualification Data Availability:	Aug 28, 2006
Date Customer Must be Ready to Receive Post-Conversion Material:	Sep 11 , 2006
Date of First Availability of Post-Conversion Material:	Sep 11 , 2006

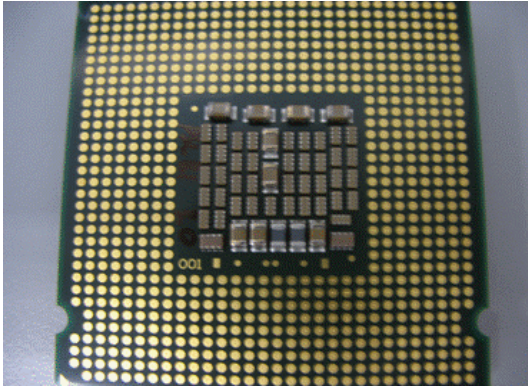
The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

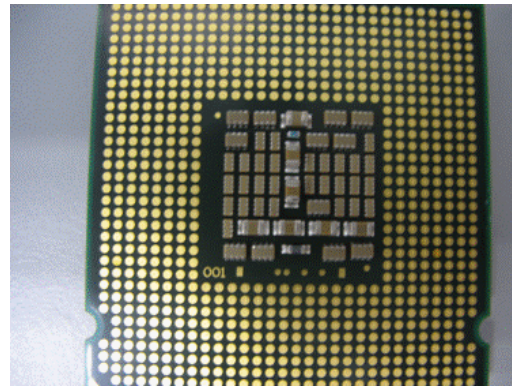
Intel® Pentium® D Processors 960, 950, 945, 925 and 915 will undergo the following changes for the C-1 to D-0 processor stepping conversion:

- CPUID will change from F64 to F65
- New S-specs for converting products
- Converting products will cross-ship in the Original and the Alternate Equivalent package. While the packages are visually different, Intel anticipates no changes to customer platforms designed to Intel guidelines (Please see below)

Original Package

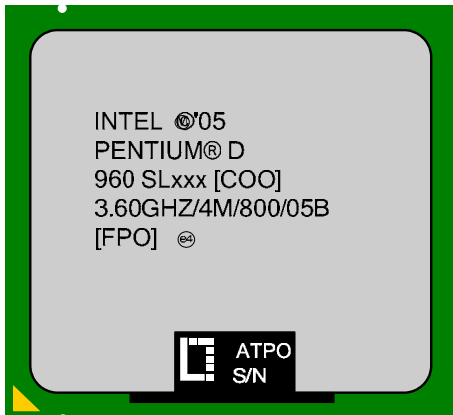


Alternate Equivalent Package

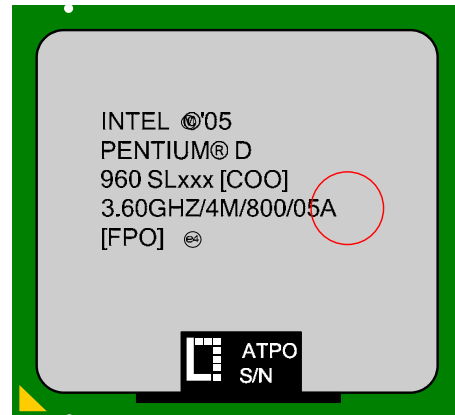


- The Pentium D Processor 960 will change from the 2005 Performance FMB (130W) to the 2005 Mainstream FMB (95W). The processor top marking will be updated as illustrated below.

C-1 Step



D-0 Step



Please refer to the *Intel® Pentium® D Processor 900 Sequence and Intel® Pentium® Processor Extreme Edition 955, 965 Datasheet* and the *Intel® Pentium® D Sequence and Intel® Pentium® Processor Extreme Edition 955, 965 Processor 900 Specification Update – NDA* for the latest specification information.

Customer Impact of Change and Recommended Action:

New BIOS update will be required for the D-0 step. Intel recommends incremental validation based on the changes listed above. This may include, but not limited to, basic functional testing, thermal/acoustics, BIOS and confidence tests using major operating systems.

Samples of the Pentium D 960 and 945 will be provided for customer validation purposes. Pentium D 960 samples will be frequency limited, but unlocked, to support validation of both the Pentium D 960 and 950. Pentium D 945 samples will be frequency limited, but unlocked, to support validation of the Pentium D 945, 925 and 915. Intel also recommends that customers validate the 945 samples at 3.2 GHz in anticipation of possible future product introductions. Customers will receive a mixture of Original Package and Alternate Equivalent Package samples.

Products Affected / Intel Ordering Codes:

Product Code	Processor Number	Product Description	Pre-Conversion Production Units		Original Package Post-Conversion Qual Samples		Alt Equip Package Post-Conversion Qual Samples		Post-Conversion Production Units	
			S-Spec	MM #	QDF #	MM #	QDF #	MM #	S-Spec	MM #
HH80553PG1044M	960	3.60 GHz VT	S L9AP	880362	QQDH	882022	QQEG	882048	SL9K7	882893
HH80553PG0964M	950	3.4 GHz VT	S L95V	878855					SL9K8	882892
HH80553PG0964MN	945	3.4 GHz non-VT	S L9QB	883951	QRLZ	883339	QRLS	883329	SL9QQ	884220
HH80553PG0804MN	925	3.0 GHz non-VT	S L9D9	881355					SL9KA	882895
HH80553PG0724MN	915	2.80 GHz non-VT	S L9DA	881356					SL9KB	882896

Reference Documents / Attachments:

Document: _____ **Location #:** _____

PCN Revision History:

Date of Revision:
June 13, 2006

Revision Number:
00

Reason:
Originally Published PCN