

**POLOVODIČE**  
akciová společnost

# T 918-1770-18

## Phase Control Thyristor

### Properties

- High operational capability
- Possibility of serial and parallel connection

### Applications

- Controlled rectifiers
- AC drives

### Key Parameters

|                    |   |        |    |
|--------------------|---|--------|----|
| $V_{DRM}, V_{RRM}$ | = | 1 800  | V  |
| $I_{TAVm}$         | = | 1 825  | A  |
| $I_{TSM}$          | = | 26 250 | A  |
| $V_{TO}$           | = | 0.965  | V  |
| $r_T$              | = | 0.170  | mΩ |

### Types

|               | $V_{RRM}, V_{DRM}$  |
|---------------|---|
| T 918-1770-18 | 1 800 V   |
| T 918-1770-16 | 1 600 V   |
| T 918-1770-14 | 1 400 V   |
| Conditions:   | $T_j = -40 \div 125 \text{ }^\circ\text{C}$ ,<br>half sine waveform,<br>$f = 50 \text{ Hz}$ |

### Mechanical Data

|       |                           |           |
|-------|---------------------------|-----------|
| $F_m$ | Mounting force            | 22 ± 2 kN |
| $m$   | Weight                    | 0.48 kg   |
| $D_s$ | Surface creepage distance | 25 mm     |
| $D_a$ | Air strike distance       | 13 mm     |

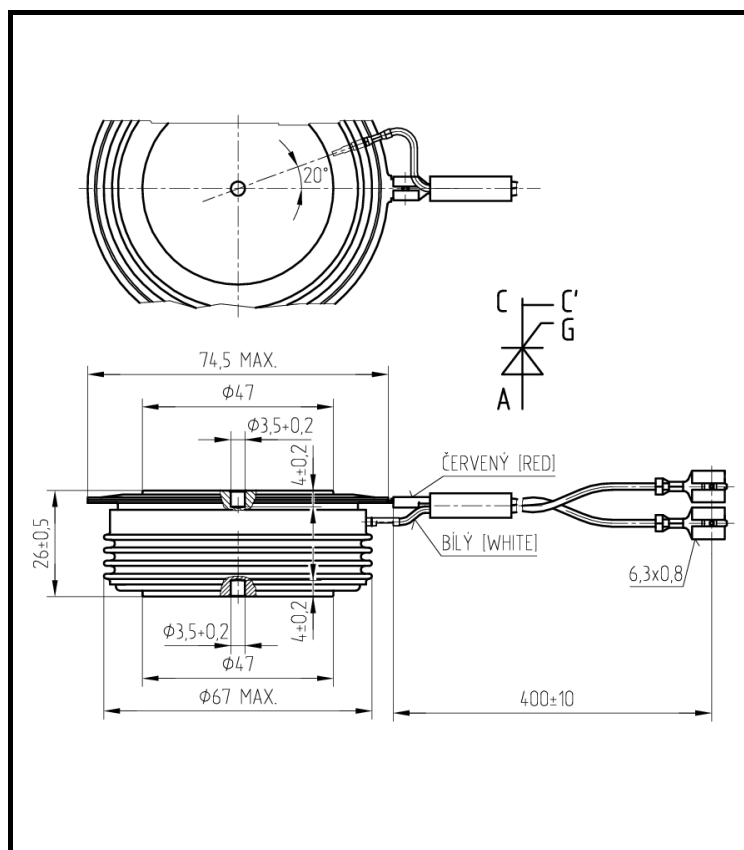


Fig. 1 Case

## Polovodice, a.s.

Novodvorska 1768/138a, 142 21 Praha 4, Czech Republic

tel.: +420 261 306 357 fax: +420 261 306 307, <http://www.polovodice.cz>

| <b>Maximum Ratings</b>    |   |   | <b>Maximum Limits</b>   | <b>Unit</b>      |
|---------------------------|---|---|-------------------------|------------------|
| $V_{RRM}$<br>$V_{DRM}$    | <b>Repetitive peak reverse and off-state voltage</b><br>$T_j = -40 \div 125 \text{ }^\circ\text{C}$   | T 918-1770-18<br>T 918-1770-16<br>T 918-1770-14 | 1 800<br>1 600<br>1 400 | V                |
| $I_{TRMS}$                | <b>RMS on-state current</b><br>$T_c = 70 \text{ }^\circ\text{C}$ , half sine waveform, $f = 50 \text{ Hz}$  |   | 2 867                   | A                |
| $I_{TAVm}$                | <b>Average on-state current</b><br>$T_c = 70 \text{ }^\circ\text{C}$ , half sine waveform, $f = 50 \text{ Hz}$  |   | 1 825                   | A                |
| $I_{TSM}$                 | <b>Peak non-repetitive surge</b><br>half sine pulse, $V_R = 0 \text{ V}$  | $t_p = 10 \text{ ms}$<br>$t_p = 8.3 \text{ ms}$ | 26 250<br>28 000        | A                |
| $I^2t$                    | <b>Limiting load integral</b><br>half sine pulse, $V_R = 0 \text{ V}$   | $t_p = 10 \text{ ms}$<br>$t_p = 8.3 \text{ ms}$ | 3 445 313<br>3 250 000  | A <sup>2</sup> s |
| $(di_T/dt)_{cr}$          | <b>Critical rate of rise of on-state current</b><br>$I_T = I_{TAVm}$ , half sine waveform, $f = 50 \text{ Hz}$ ,<br>$V_D = 2/3 V_{DRM}$ , $t_r = 0.3 \text{ } \mu\text{s}$ , $I_{GT} = 2 \text{ A}$ |   | 200                     | A/ $\mu\text{s}$ |
| $(dv_D/dt)_{cr}$          | <b>Critical rate of rise of off-state voltage</b><br>$V_D = 2/3 V_{DRM}$  |   | 1 000                   | V/ $\mu\text{s}$ |
| $P_{GAVm}$                | <b>Maximum average gate power losses</b>  |   | 3                       | W                |
| $I_{FGM}$                 | <b>Peak gate current</b>  |   | 10                      | A                |
| $V_{FGM}$                 | <b>Peak gate voltage</b>  |   | 12                      | V                |
| $V_{RGM}$                 | <b>Reverse peak gate voltage</b>  |   | 10                      | V                |
| $T_{jmin} - T_{jmax}$     | <b>Operating temperature range</b>  |   | -40 $\div$ 125          | $^\circ\text{C}$ |
| $T_{stgmin} - T_{stgmax}$ | <b>Storage temperature range</b>  |   | -40 $\div$ 125          | $^\circ\text{C}$ |

Unless otherwise specified  $T_j = 125 \text{ }^\circ\text{C}$

| Characteristics |  | Value  |       |                   | Unit       |
|-----------------|--|--|-------|-------------------|------------|
|                 |  | min.   | typ.  | max.              |            |
| $V_{TM}$        | <b>Maximum peak on-state voltage</b><br>$I_{TM} = 2\ 000\ A$   |  |       | 1.300             | V          |
| $V_{T0}$        | <b>Threshold voltage</b>   |  |       | 0.965             | V          |
| $r_T$           | <b>Slope resistance</b><br>$I_{T1} = 2\ 289\ A, I_{T2} = 6\ 866\ A$  |  |       | 0.170             | m $\Omega$ |
| $I_{DM}$        | <b>Peak off-state current</b><br>$V_D = V_{DRM}$   |  |       | 150               | mA         |
| $I_{RM}$        | <b>Peak reverse current</b><br>$V_R = V_{RRM}$   |  |       | 150               | mA         |
| $t_{gd}$        | <b>Delay time</b><br>$T_j = 25\ ^\circ C, V_D = 0.4\ V_{DRM}, I_{TM} = I_{TAVm},$<br>$t_r = 0.3\ \mu s, I_{GT} = 2\ A$ |  |       | 2                 | $\mu s$    |
| $t_q$           | <b>Turn-off time</b><br>$I_T = 2\ 000\ A, di_T/dt = 12.5\ A/\mu s,$<br>$V_D = 2/3\ V_{DRM}, dv_D/dt = 50\ V/\mu s$     |  | 150   |                   | $\mu s$    |
| $Q_{rr}$        | <b>Recovery charge</b><br>the same conditions as at $t_q$  |  | 2 200 |                   | $\mu C$    |
| $I_H$           | <b>Holding current</b>   | $T_j = 25\ ^\circ C$<br>$T_j = 125\ ^\circ C$                          |       | 170<br>90         | mA         |
| $I_L$           | <b>Latching current</b>  | $T_j = 25\ ^\circ C$<br>$T_j = 125\ ^\circ C$                          |       | 450<br>350        | mA         |
| $V_{GT}$        | <b>Gate trigger voltage</b><br>$V_D = 12V, I_T = 4\ A$   | $T_j = -40\ ^\circ C$<br>$T_j = 25\ ^\circ C$<br>$T_j = 125\ ^\circ C$ | 0.25  | 4<br>3<br>2       | V          |
| $I_{GT}$        | <b>Gate trigger current</b><br>$V_D = 12V, I_T = 4\ A$   | $T_j = -40\ ^\circ C$<br>$T_j = 25\ ^\circ C$<br>$T_j = 125\ ^\circ C$ | 10    | 500<br>250<br>150 | mA         |

Unless otherwise specified  $T_j = 125\ ^\circ C$

| Thermal Parameters |   | Value | Unit |
|--------------------|---|-------|------|
| $R_{thjc}$         | <b>Thermal resistance junction to case</b><br>double side cooling | 16.0  | K/kW |
|                    | anode side cooling  | 25.0  |      |
|                    | cathode side cooling  | 45.0  |      |
| $R_{thch}$         | <b>Thermal resistance case to heatsink</b><br>double side cooling | 4.0   | K/kW |
|                    | single side cooling   | 8.0   |      |

Polovodice, a.s., Novodvorska 1768/138a, 142 21 Praha 4, Czech Republic

Polovodice, a.s. reserves the right to change the data contained herein at any time without notice

**Transient Thermal Impedance**

Analytical function for transient thermal impedance

$$Z_{thjc} = \sum_{i=1}^4 R_i (1 - \exp(-t / \tau_i))$$

Conditions:

$F_m = 22 \pm 2$  kN, Double side cooled

Correction for periodic waveforms

- 180° sine: add 1.3 K/kW
- 180° rectangular: add 1.8 K/kW
- 120° rectangular: add 3.0 K/kW
- 60° rectangular: add 5.1 K/kW

|              |        |        |        |        |
|--------------|--------|--------|--------|--------|
| <i>i</i>     | 1      | 2      | 3      | 4      |
| $\tau_i$ (s) | 0.4653 | 0.1533 | 0.0375 | 0.0034 |
| $R_i$ (K/kW) | 5.50   | 7.24   | 2.00   | 1.34   |

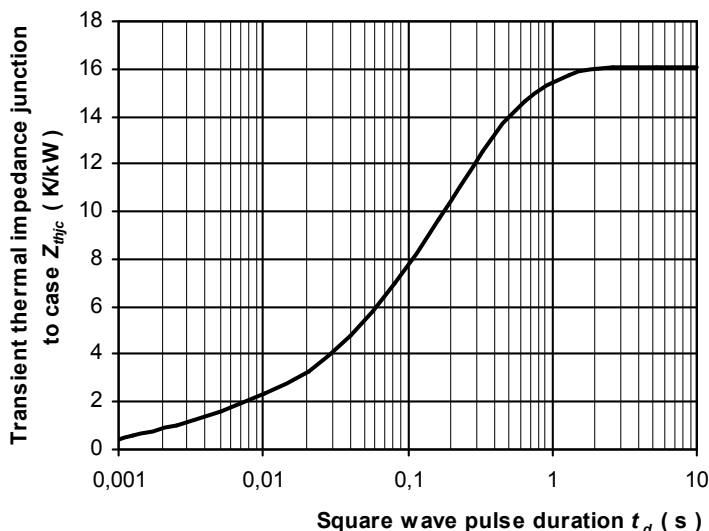


Fig. 2 Dependence transient thermal impedance junction to case on square pulse

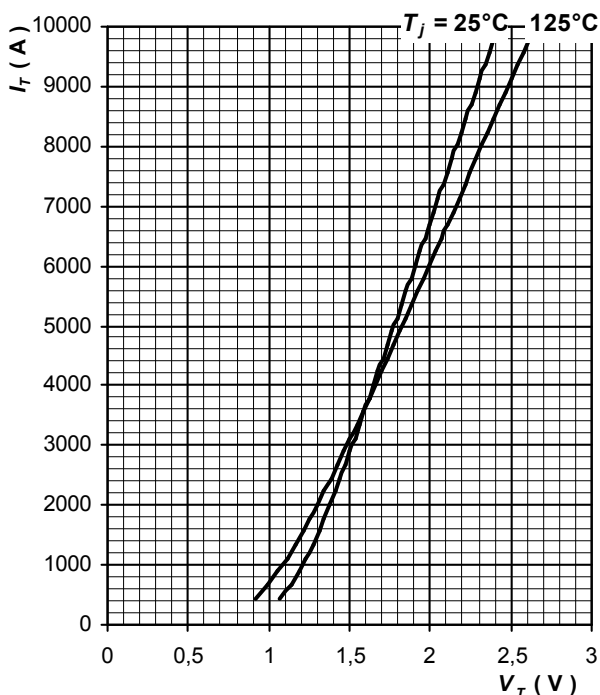


Fig. 3 Maximum on-state characteristics

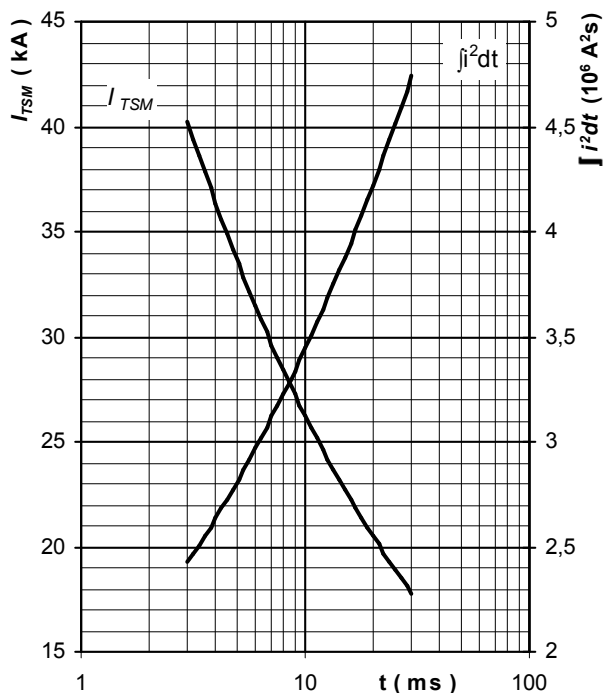


Fig. 4 Surge on-state current vs. pulse length, half sine wave, single pulse,  $V_R = 0$  V,  $T_j = T_{jmax}$

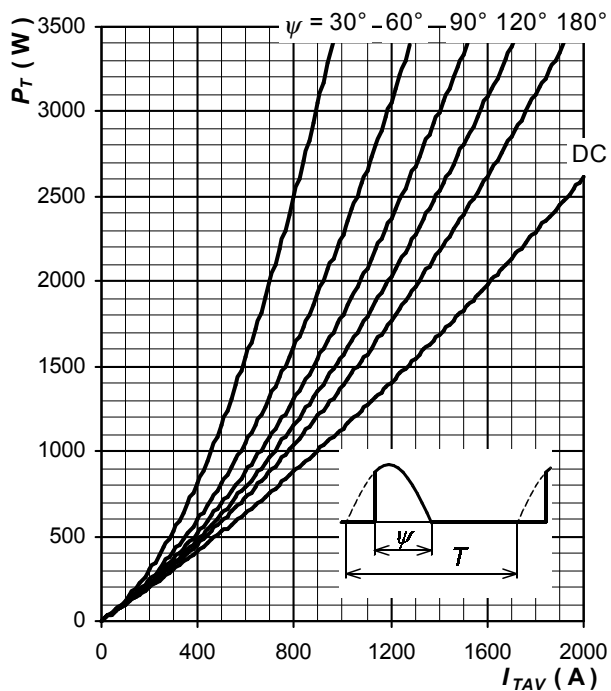


Fig. 5 On-state power loss vs. average on-state current, sine waveform,  $f = 50 \text{ Hz}$ ,  $T = 1/f$

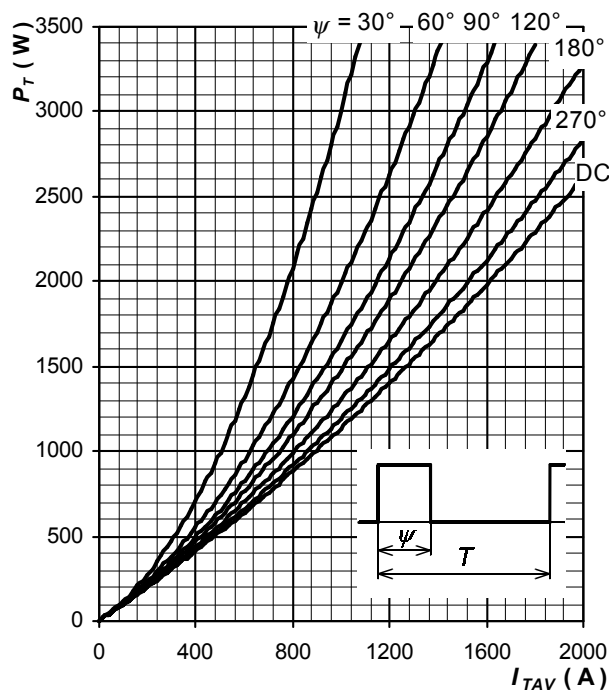


Fig. 6 On-state power loss vs. average on-state current, square waveform,  $f = 50 \text{ Hz}$ ,  $T = 1/f$

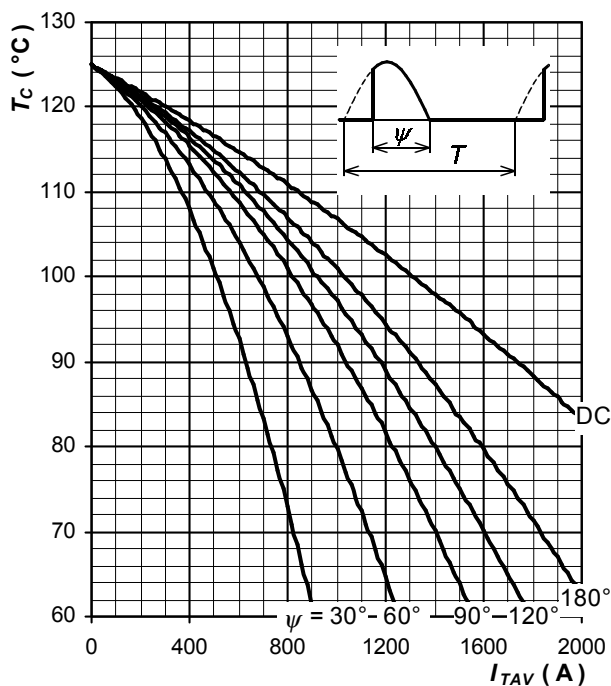


Fig. 7 Max. case temperature vs. aver. on-state current, sine waveform,  $f = 50 \text{ Hz}$ ,  $T = 1/f$

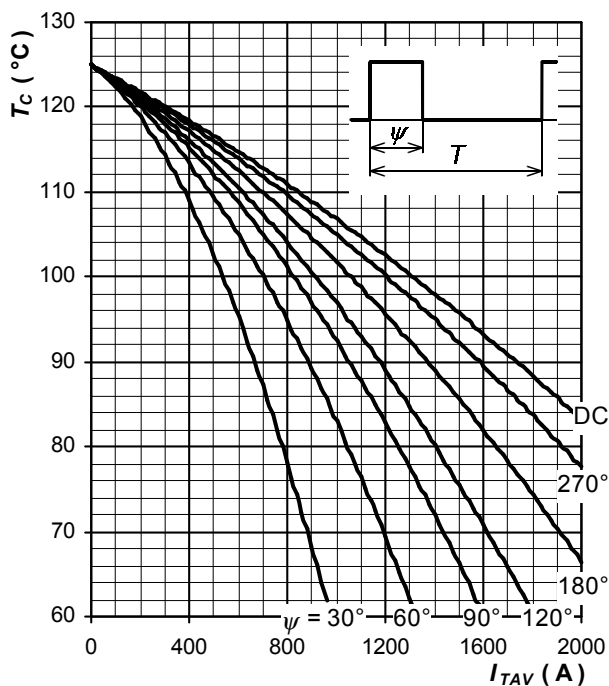


Fig. 8 Max. case temperature vs. aver. on-state current, square waveform,  $f = 50 \text{ Hz}$ ,  $T = 1/f$

Notes

Polovodice, a.s., Novodvorska 1768/138a, 142 21 Praha 4, Czech Republic