Two-board array processor features MP/M-86-based support software

For use with Multibus microcomputer systems, Sky Computers' SKYMNK-M is the latest model in the company's SKYMNK family of programmable, 32-bit, floating-point array processors. The SKYMNK-M is packaged on two single-board computer modules that plug directly into the Multibus backplane.

Array processors are used widely in applications which require high-speed, repetitive number crunching. Signal processing, robotics, seismic exploration, graphics, and numerical and statistical analysis all demand fast and accurate floating-point arithmetic performed on large data sets.

Users in these fields often require onemegaflop number crunching. According to Sky, however, many have been unable to afford the hardware required, usually a large mainframe or a minicomputer with an attached array processor. The SKYMNK-M gives these users a low-cost alternative, claims the company.

The SKYMNK-M features one million floating-point operations per second, 32-bit floating-point arithmetic conforming to the proposed IEEE standard format, 48-bit extended precision for selected computations, and real, complex, and integer arithmetic instructions. It also shares memory with the host to reduce system overhead, and can directly address up to 16M bytes. A vector subroutine library of Fortran-callable math functions is provided and includes FFT, convolution/correlation, and thresholding.

The SKYMNK-M is tightly coupled to the host microcomputer via the Multibus, which enables it to share the host's memory and operate under the control of the, host's operating system. This approach eliminates the need for special memory as part of the array processor and makes it unnecessary to move data between processors before each function, says Sky. With the SKYMNK-M performing floating-point arithmetic directly on data in the host memory, the host becomes a self-contained supermicrocomputer system operating at speeds over 100 times that of the micro alone, according to the company.

MP/M-86-based support software is provided with the SKYMNK-M. The vector subroutine library enables users to invoke the SKYMNK-M array processor with simple calls from host Fortran or assembly language programs.

The SKYMNK-M is priced under \$4000 in OEM quantities and is available for delivery 30 days after receipt of order.

Reader Service Number 19

Three-inch floppy disk product announced

A new, compact floppy disk, with dimensions of $80 \times 100 \times 5$ millimeters (about 3×4 inches), has been jointly announced by Maxell, Hitachi, and Matsushita. Packaged in a hard plastic housing, it will be used in disk drive systems that are compatible and interchangeable with conventional floppy disk drives.

The disk will be marketed by Maxell, but all three companies will join in proposing the new format to disk and hardware manufacturers. Ted Ozawa, vice-president for special products at Maxell, stated, "We believe that this new standard for a 3-inch compact floppy disk will be adopted by many hardware manufacturers and we expect that various disk drives using 3-inch disks will begin to appear in the latter part of 1982."

The capacity of the new disk is 125K bytes for the single-sided, single-density version and 500K bytes for the double-sided, doubledensity version. In modified frequency modulated recording mode, the disk features a recording density of 9000 bpi and a track density of 100 tpi, with 40 tracks per side. It will support a data transfer rate of 250K bits per second at 300 rpm.

Maxell anticipates US availability of the new disks in fall 1982.

Reader Service Number 21



The Super/net, an S-100 single-board computer, includes 64K of bank-select dynamic RAM, a Z80A CPU, 2K of monitor EPROM, a 5.25- and 8-inch floppy disk controller, two serial and two parallel interface ports, and a Z80A CTC for real-time interrupts. The board, manufactured by Advanced Micro Digital Corporation, supports DMA and operates under both CP/M and MP/M. Designed to meet the specifications of the 1979 draft of the proposed IEEE-696 (S-100) standard, the board is available at a suggested retail price of \$1125.

Reader Service Number 20



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