

Branching Time Logics & Flat Counter Systems

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Verification > Model Checking

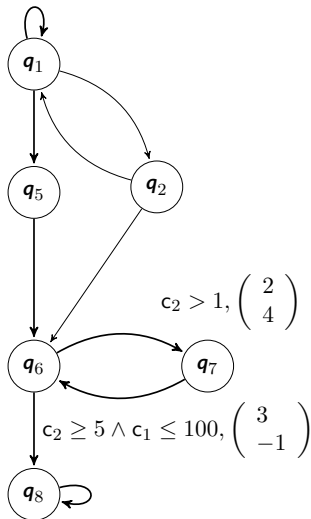
Model-Checking $\{\text{CTL}^*, \text{CTL}, \text{CTL}_{\text{EF}}\}$

over Flat Counter Systems

is Equivalent to

Satisfiability of Presburger Arithmetic.

Models > Counter Systems



Counters : $\{c_1, c_2, \dots, c_n\}$

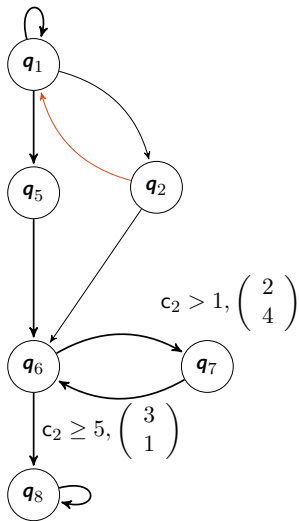
Updates : $\mathbf{u} \in \mathbb{Z}^n$.

Guards : Boolean Combination of arithmetic constraints

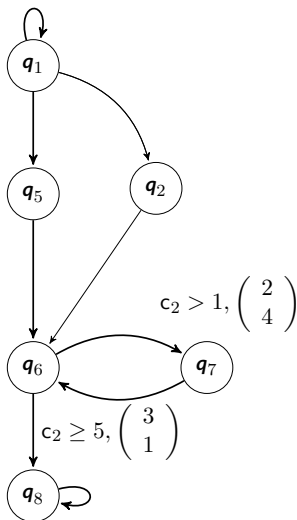
$2 \cdot c_1 + 5 \cdot c_2 - c_3 \in \{\leq, \geq, <, >\} 5$.

Models > Flat Counter Systems

No intersecting/nested loops in the structure.



Non-Flat



Flat

Models > Flat Counter Systems

Can still be used to model some systems e.g. Broadcast Protocols

[Finkel, Leroux - FSTTCS'02, Fribourg, Olsén - LOPSTR'96]

Under-approximation of model-checking of counter systems.

[Boigelot - 98, Comon, Jurski - CAV'98, Leroux, Sutre - ATVA'05]

Decidable Model checking for some logics (Presburger CTL*).

[Demri et al. - JANCL'10]

Optimal complexity of Model checking for many linear-time logics known (LTL with Past, FO, linear μ -calculus).

[Demri, Dhar, sangnier - IJCAR'12, Demri, Dhar, Sangnier - ICALP'13]

Checking safety property on flat systems with octagonal loop is NP-Complete.

[Bozga, Iosif, Konceny - VMCAI'14]

Specification > Syntax

Computation Tree Logic (CTL)

$\phi := p \mid g \mid \neg\phi \mid \phi \vee \phi \mid EX\phi \mid AX\phi \mid E[\phi U \phi] \mid A[\phi U \phi]$.

Computation Tree Logic* (CTL*)

$\phi := p \mid g \mid \neg\phi \mid \phi \vee \phi \mid X\phi \mid \phi U \phi \mid E\phi$.

Computation Tree Logic with only EF (CTL_{EF})

$\phi := p \mid g \mid \neg\phi \mid \phi \vee \phi \mid EF\phi$.

★ Each contains counter constraints

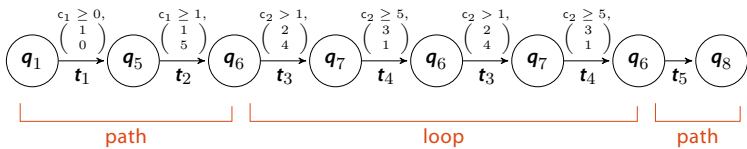
Problem > Model Checking

MC (L, FCS)

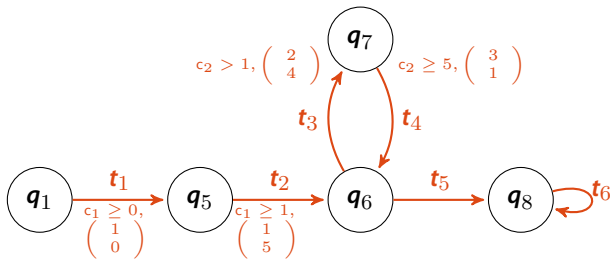
INPUT : A flat counter system \mathbf{s} , a specification \mathcal{A} in logic L,
a configuration $\langle \mathbf{q}_0, \mathbf{v}_0 \rangle$.

OUTPUT : Does there exists an execution ρ starting with $\langle \mathbf{q}_0, \mathbf{v}_0 \rangle$
in \mathbf{s} such that $\rho, 0 \models \mathcal{A}$?

Simpler Models > Path Schemas



Simpler Models > Path Schemas



- Path Schemas - an alternating sequence of paths and loops -
$$P = (t_1 t_2)(t_3 t_4)^+(t_5)(t_6)^\omega$$
- A concise way of representing infinite runs = $\langle \text{Path schema}, \mathbf{m} \rangle$
 - ▶ \mathbf{m} denotes the number of times loops are taken - $\langle P, (2) \rangle$
- At most exponentially many *minimal* path schemas in flat counter systems [Leroux, Sutre - ATVA'05].

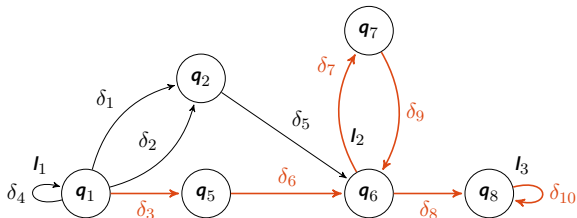
$MC(CTL^*, FCS) \triangleright$ Reduction

$MC(CTL^*, FCS)$

Reduction
(modulo LogSpace)

Satisfiability of Presburger Arithmetic

MC(CTL*,FCS) > Encoding Run

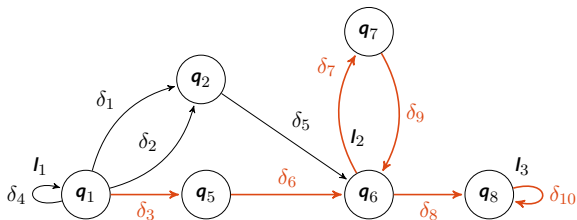


$$\delta_3 \cdot \delta_6 \cdot (l_2)^{146} \cdot \delta_8 \cdot (l_3)^\omega = \begin{cases} \mathbf{v}_p = (3, 6, 2, 8, 3, 0, 0, 0, 0, 0, 0, 0, 0) \\ \mathbf{v}_t = (0, 0, 1, 0, 1, 0, 0, 0, 0, 0, 0, 0, 0) \\ \mathbf{v}_{it} = (1, 1, 146, 1, 0, 0, 0, 0, 0, 0, 0, 0, 0) \end{cases}$$

ϕ_{ps} - Characterizing the properties of path schema

$$\bigvee_{i=1}^8 ((x_t^i = 1 \wedge x_t^{i+2} = 1) \wedge (x_p^i > 0 \wedge x_p^{i+2} > 0)) \Rightarrow (x_t^{i+1} = 0)$$

MC(CTL*,FCS) > Encoding Run

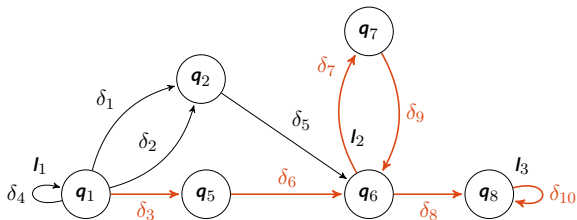


$$\delta_3 \cdot \delta_6 \cdot (l_2)^{146} \cdot \delta_8 \cdot (l_3)^\omega = \begin{cases} \mathbf{v}_p = (3, 6, 2, 8, 3, 0, 0, 0, 0, 0, 0, 0, 0) \\ \mathbf{v}_t = (0, 0, 1, 0, 1, 0, 0, 0, 0, 0, 0, 0, 0) \\ \mathbf{v}_{it} = (1, 1, 146, 1, 0, 0, 0, 0, 0, 0, 0, 0, 0) \end{cases}$$

ϕ_{run} - Characterizing the runs through path schema

$$\forall i > 0. \text{update}(\mathbf{v}_p, \mathbf{v}_t, \mathbf{v}_{it})[1 \dots i] \geq 0$$

MC(CTL*,FCS) > Encoding Run



$$\delta_3 \cdot \delta_6 \cdot (l_2)^{146} \cdot \delta_8 \cdot (l_3)^\omega = \begin{cases} \mathbf{v}_p = (3, 6, 2, 8, 3, 0, 0, 0, 0, 0, 0, 0, 0) \\ \mathbf{v}_t = (0, 0, 1, 0, 1, 0, 0, 0, 0, 0, 0, 0, 0) \\ \mathbf{v}_{it} = (1, 1, 146, 1, 0, 0, 0, 0, 0, 0, 0, 0, 0) \end{cases}$$

ϕ_{CTL^*} - Encoding the CTL* formula [Demri et al. - JANCL'10]

$$\phi = \exists x_p^1 \cdots x_p^{10}, x_t^1 \cdots x_t^{10}, x_{it}^1 \cdots x_{it}^{10} . (\phi_{ps} \wedge \phi_{run} \wedge \phi_{\text{CTL}^*})$$

MC(CTL*,FCS) > Complexity

- ▶ Polynomial-time reduction compared to exponential time reduction known from [\[Demri et al. - JANCL'10\]](#).
 - ▶ No enumeration of path schemas in formula.
 - ▶ Encoding runs using a constant number of fixed size integer vectors.
 - ▶ Utilizing the power of quantifiers in an essential way.

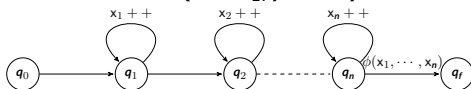
MC(CTL_{EF}, FCS) > Reduction

Satisfiability of Presburger Arithmetic

$$Q_1 x_1 Q_2 x_2 \cdots Q_n x_n \phi(x_1, x_2, \dots, x_n)$$

Reduction
(modulo LogSpace)

MC(CTL_{EF}, FCS)



\exists	EF
\forall	AG

Branching-Time > Overview

$MC(\text{CTL}^*, \text{FCS})$

\updownarrow

$MC(\text{CTL}, \text{FCS})$

\updownarrow

$MC(\text{CTL}_{\text{EF}}, \text{FCS})$

\updownarrow

Satisfiability of Presburger arithmetic

[RP'14]

$MC(\text{Modal } \mu\text{-calculus}, \text{FCS})$??

■ That's It > Questions?

Thank You
For Your Kind Attention