

## Intel<sup>®</sup> Desktop Board DP55KG and Intel<sup>®</sup> Desktop Board DP55SB Extreme Series

Performance Tuning Guide

Revision 1.4

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## 1. Introduction

Performance tuning on Intel<sup>®</sup> Desktop Board DP55KG and Intel<sup>®</sup> Desktop Board DP55SB can be a simple and straightforward process, providing useful gains in overall system performance. This guide describes ways to increase system performance by tuning the various subsystems of the desktop board. The main focus areas include the Intel<sup>®</sup> Core<sup>™</sup> i7-800 and Intel<sup>®</sup> Core<sup>™</sup> i5-700 processor series, the memory subsystem, and the data interlinks connecting the various desktop board components. The procedures and examples in this document are for reference only and are not guaranteed to work in all situations and system configurations.

Please note that operating desktop board components beyond their nominal values adds additional thermal stress to the overall system and additional cooling may be required to ensure stable system operation.

It is also important to note that over-clocking any desktop board can create additional electrical and thermal stress on the components and can reduce the system's overall useful lifespan. Consequently, over-clocking on an Intel<sup>®</sup> Desktop Board Extreme Series is not covered under the standard warranty.

Performance tuning is not an exact science; system stability is highly dependent upon overall system construction and will vary with each system configuration.

During the lifetime of the board the BIOS may be updated with additional features and improvements like the look and feel.

### 1.1. Intel<sup>®</sup> Desktop Control Center

The Intel<sup>®</sup> Desktop Control Center is a great tool for monitoring most Intel Desktop Board Extreme Series-based systems and is available for download at <u>http://www.intel.com/go/dcc</u>. New to Intel Desktop Board DP55KG/DP55SB is the easy auto tune capability. Beyond the scope of this guide, the Intel Desktop Control Center (Figure 1) is another resource for performance tuning.



Figure 1. Intel Desktop Control Center

## Intel<sup>®</sup> Core<sup>™</sup> i7 and Intel<sup>®</sup> Core i5<sup>™</sup> Processors and Intel<sup>®</sup> P55 Express Chipset General Concepts

### 2.1. Architecture

The Intel Desktop Board DP55KG/DP55SB platform consists of an Intel Core i7-800 or Intel Core i5-700 processor series and the Intel<sup>®</sup> P55 Express Chipset component—the Intel<sup>®</sup> P55 Platform Controller Hub (PCH). For a detailed description of the complete platform, refer to the *Intel Desktop Board DP55KG Technical Product Specification* or the *Intel Desktop Board DP55SB Technical Product Specification* available at http://www.intel.com/products/desktop/motherboard.

The processor now integrates the system memory controller and accesses DDR3 memory through two independent memory channels. The processor also provides support for one full PCI Express\* 2.0 x16 graphics slot or switches to two PCI Express 2.0 x8 slots. The processor connects to the PCH via the Direct Media Interface (DMI) interconnect bus. The PCH provides support for SATA (3.0 Gb/s), USB 2.0, and other system interfaces. Figure 2 shows a block diagram of the Intel Desktop Board DP55KG/DP55SB platform.

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Figure 2. Intel Desktop Board DP55KG/DP55SB Block Diagram

All desktop board components are driven from a single 133 MHz host clock (BCLK). The resulting processor frequency is generated by applying a multiplier value to the host clock (BCLK).

There are three frequency multipliers on these desktop boards. All frequencies are tied to the host clock (BCLK) which is used to develop the overall system speed. The multipliers are as follows:

- 1. Processor: The processor frequency is developed when the system host clock frequency (default 133 MHz) is multiplied by the Processor Multiplier. The Processor Multiplier automatically adjusts higher with Turbo Mode or lower with EIST.
- Memory: The memory frequency is developed when the Memory Multiplier is multiplied by the system host clock frequency. For example, a Memory Multiplier of 10 x 133 MHz host clock results in a memory frequency of 1333 MHz.
- 3. Uncore: Applies to the non-core related items in the processor, such as the cache controller, memory controller, and the data and address buffers. The Uncore Multiplier is locked by the processor at 18 for an Intel Core i7-870 processor for example. The Uncore Multiplier is unlocked on Intel Core i7-900 series Extreme Edition processors for example.

## 3. Intel Desktop Board DP55KG/DP55SB BIOS Setup

This section describes examples of modifying system controls and the performance tuning options that are available to enhance desktop board performance.

### 3.1. Accessing BIOS Setup

The BIOS setup screen can be accessed at system startup by pressing the F2 key at the setup screen prompt. It is advised to run the latest BIOS version to ensure that performance features are at the highest level of optimization. The latest version BIOS can be obtained from <a href="http://downloadcenter.intel.com">http://downloadcenter.intel.com</a>.

Before starting any performance tuning, be sure that the BIOS setup defaults have been applied by pressing the F9 key while in BIOS setup mode. Press the F10 key to save the settings once the BIOS changes have been made.

### 3.2. Recovering from an Unstable System

Should performance values be set beyond the point of stable system operation, there are three options for recovery:

- 1. Third-Generation Watchdog Timer
- 2. Back-to-BIOS button
- 3. BIOS configuration jumper

A third-generation hardware Watchdog Timer has been included as part of the desktop board circuitry. This timer is enabled by default in the BIOS and will automatically boot to BIOS with a safe set of performance values whenever it detects unstable system operation or if the board hangs during start up.

If the Watchdog Timer fails to automatically reboot the board, the Back-to-BIOS button located on the back panel can be depressed to cause the board to boot into the Back-to-BIOS screen with default BIOS settings invoked while maintaining all of the user's BIOS settings shown to allow for easy recovery.

In addition, the BIOS configuration jumper is provided on the board to force the board to boot in a safe mode making it possible to reset the BIOS to default settings and restore the board to functionality. Refer to the desktop board product guide for instructions on using the jumper for resetting purposes.

### 3.3. Preparing Setup Options (Optional)

It is recommended to disable non-essential interfaces (such as, onboard audio, USB, LAN, external SATA, etc.) when tuning to get a clean and basic system as a starting point for performance tuning. Once the performance variables have been optimized, the other onboard features can then be re-enabled as needed.

Scroll to "Configuration" and use the submenu to disable these features (Figure 3).



Figure 3. System Setup with Features Disabled (Optional)

Figure 4 shows the System Power submenu with the CPU C states disabled for example.

*Note:* 1-, 2-, and 3-Core Turbo modes require CPU C-states. Disabling C-states will limit Core Turbo to only the 4-Core ratio. For example, an Intel Core i7-870 processor with a 4-Core Turbo ratio results in a maximum frequency of only 3.2 GHz.

	System Setup		
Main Configuration Performance	Security Power Boot 1	Exit	
Intel® Dynamic Power Technology Enhanced Intel SpeedStep® Tech CPU C State	<enable> <mark><enable></enable></mark></enable>		Allows system to put CPU Cores to sleep for power saving. Disabling has an
System Power Options			effect on Turbo Mode.
After Power Failure	<stay off=""></stay>		
Wake on LAN from S5	<stay off=""></stay>		
ACPI Suspend State	<s3 state=""></s3>		
S3 State Indicator	<blink></blink>		
S1 State Indicator			
Wake System from S5	(Dis Disable		
PCIE HSPM Support	<b>VDIS</b> LMADIE		
			†↓=Select Item PgUp/PgDn=Scroll List
			Enter=Change Setting Esc=Discard Changes

Figure 4. System Power with CPU C States Disabled (Optional)

### 4. Optimizing Processor Performance

Performance tuning options are located in a menu section labeled "Performance." Read and agree to the disclaimer (Figure 5). Please note that any performance increase achieved is dependent on the Processor, memory and motherboard used. All of these parts of the system have inherent variability and one or more will limit the overall systems ultimate top performance.



Figure 5. Performance Tuning Disclaimer

### 4.1. Processor Performance Tuning Options

The main Performance Tuning page is shown in Figure 6 with an Intel Core i7-870 processor installed.

	System	n Setup		
Main Configuration <mark>Performance</mark>	Security Power	r Boot Ex	xit	
Failsafe Watchdog Host Clock Frequency Override	<enable> <automatic< th=""><th>:&gt;</th><th></th><th></th></automatic<></enable>	:>		
Host Clock Frequency Processor Overrides Intel® Turbo Boost Technology Core Max Multiplier Speed	<b>Default</b> 133 MHz Enable 27 3.60 GHz	Proposed 133 MHz Enable 27 3.60 GHz	Active 133 MHz Enable 27 3.60 GHz	
Memory Overrides Multiplier Speed Uncore Multiplier Uncore Speed Bus Overrides	10 1333 MHz 18 2.40 GHz	10 1333 MHz 18 2.40 GHz	10 1333 MHz 18 2.40 GHz	
Voltages Processor Core Memory Processor Uncore PCH Core	Dynamic 1.5000V 1.1000V 1.0300V	Dynamic 1.5000V 1.1000V 1.0300V	Dynamic 1.5000V 1.1000V 1.0300V	↔=Select Screen ↑↓=Select Item Enter=Select Submenu F9=Setup Defaults F10=Save and Exit Esc=Previous Page
			Changes	

Figure 6. Performance Tuning Main Page

CPU tuning options are listed under "Processor Overrides" as shown in Figure 7. Refer to Table 1 for descriptions of the Processor Overrides.

	System Setup		
Performance			
Processor Overrides		A selection of Nome	
<b>CPU Voltage Override Type</b> CPU Voltage Override CPU Vreg Droop Control	<mark><none></none></mark> <default> <high (power="" saving)="" v-droop=""></high></default>	it's own power usage with default upper limits.	
Maximum Non-Turbo Ratio	[22]	will keep the CPU at a specific user	
CPU Idle State	<low power=""></low>	specified voltage at all times.	
Intel® Turbo Boost Technology TDC Current Limit Override (Amps) TDP Power Limit Override (Watts) 4-Core Ratio Limit 3-Core Ratio Limit 2-Core Ratio Limit 1-Core Ratio Limit	<enable> [89] [95] [24] [24] [26] [27]</enable>	all times. A selection of Dynamic will again allow the CPU to vary it's voltage use, but with new upper limits	
		†4=Select Item Enter=Select Submenu F9=Setup Defaults F10=Save and Exit Esc=Previous Page	
	Changes		

Figure 7. Processor Overrides Page

Setup Option	on Description						
CPU	Voltage Override Options						
Static CPU Voltage Override	Overrides the voltage requested by the processor and applies a static voltage. This option disables the dynamic voltage control of the processor.						
Dynamic CPU Voltage Offset (mV)	Maintains the dynamic VID control of the processor which scales the output voltage around the selected base voltage automatically.						
CPU Vreg Droop Control	Changes the amount of voltage drop applied when the processor draws maximum current. Choosing the "Low V-droop" option causes the desktop board to supply voltage with no drop even at maximum processor current draw.						
Proce	Processor Performance Options						
Note: All multiplier values listed below are applied to the single 133 MHz Host clock.							
Maximum Non-Turbo Ratio	Sets the default multiplier that the processor will use. Useful when increasing the host clock frequency above 133 MHz. The maximum multiplier is dependent on the processor installed.						
CPU Idle State	Enables CPU low power states. High performance mode will always run the CPU at the chosen multiplier. Low power mode allows EIST to function.						
Turt	oo Mode Ov	erride Options					
TDC Current Limit Overrride (Amps)	Displays the default current limit and the processor rated TDP.						
TDP Power Limit Override (Watts)							
Turbo Mode Multipliers	Display's M	aximum CPU performance given					
4-Core Ratio Limit	adequate the applied white	le not in C3 or C6 power states.					
3-Core Ratio Limit							
2-Core Ratio Limit							

### Table 1. Processor Overrides

# 4.2. Processor Tuning Process: The Host Clock Method

The frequency of the CPU can be set by increasing the host clock and leaving the multipliers unchanged.

From the Performance page set the "Host Clock Frequency Override" setting to "Manual".

Next, increase the Host Clock Frequency (MHz) to a desired level.

	System	ı Setup		
Main Configuration <mark>Performance</mark> S	Security Power	Boot Ex	it	
Failsafe Watchdog Host Clock Frequency Override Host Clock Frequency (MHz) 	<enable> <manual> <mark>[148]</mark></manual></enable>			This overrides the host clock frequency. Host Clock Frequency x Presence Multiplier -
Host Clock Frequency Processor Overrides	Default 133 MHz	Proposed 148 MHz	Active 133 MHz	Processor Speed Host Clock Frequency x RAM multiplier = RAM
Intel® Turbo Boost Technology Core Max Multiplier Speed	Enable 27 3.60 GHz	Enable 27 4.00 GHz	Enable 27 3.60 GHz	speed Note: to increase the base clock further you might need to reduce either CPU or RAM multiplier
Memory Overrides Multiplier Speed Uncore Multiplier Uncore Speed Bus Overrides	8 1067 MHz 18 2.40 GHz	8 1186 MHz 18 2.67 GHz	8 1067 MHz 18 2.40 GHz	
Voltages Processor Core Memory Processor Uncore PCH Core	Dynamic 1.5000V 1.1000V 1.0300V	Dynamic 1.5000V 1.1000V 1.0300V	Dynamic 1.5000V 1.1000V 1.0300V	+→=Select Screen ↑↓=Select Item Enter=Select Submenu F9=Setup Defaults F10=Save and Exit Esc=Previous Page
			Changes	

#### Figure 8. Increasing the Host Clock Frequency

In Figure 8, the column labeled "Proposed" shows the results of setting the host clock to 148 MHz which will result in a processor speed of 4.00 GHz with Turbo mode set to default 27 multiplier with Turbo Boost enabled. Additional voltage may need to be applied to the processor to ensure stable performance at 4.00 GHz.

Note: The multipliers for the processor were left unchanged and only the host clock frequency (BCLK) was increased. Since the frequency of the other board subsystems is derived from the base clock, the memory speed will be increased as well, this can be seen in Figure 8 as 1186 MHz memory frequency.

This increase may affect system stability, and, therefore, memory timings and voltage adjustments in the BIOS memory section might be necessary for system stability.

Refer to Section 5 for more information on memory settings.

### 4.3. Confirming System Stability

Once the selections are made, it is time to confirm the selected frequency and gauge system stability:

- 1. Save changes using the F10 key and boot into the operating system.
- Next force the operating system to request that the processor operate in PO. PO is the highest performance state. To do so, in Windows\* XP, select the "Always On" profile. In Windows Vista\* or Windows\* 7 use the "Performance" profile, in the Power Settings Control Panel applet.
- 3. When processor cores are idle, the Intel Core i7-800 and Intel Core i5-700 processor series will operate at a reduced multiplier value. By applying a heavy load to the processor in the operating system all processor cores will be out of the idle state and run at the multiplier values chosen in the BIOS setup. Heavy loads can be applied by using any common processor benchmarking software.

*Note:* If the settings have been set too aggressively and the board becomes unstable, refer to Section 3.2 for recovery options.

Other software tools to test stability are:

Orthos (http://sp2004.fre3.com/beta/beta2.htm)

3DMark\* Vantage (http://www.futuremark.com/benchmarks/3dmarkvantage/introduction/)

Loading the processor will also serve to test the stability of the system. Prime95\*, Super PI\*, or similar software can be used to load the processor.

### 4.4. How to Measure Processor Core Frequency

Use a processor frequency display tool to verify the final speed. The Intel Desktop Control Center will have details on processor frequency and system speeds as shown in Figure 9.



Figure 9. Confirming System Levels with Intel Desktop Control Center

Third party tools are also available like CPUID CPU-Z\* or CPUID TMonitor\* to monitor processor speed.

Once the desired frequency and system stability have been confirmed, the other subsystems can be optimized.

## 5. Memory Optimization

### 5.1. Optimizing Memory Performance

A wide variety of memory timing parameters can be adjusted in the BIOS setup.

Although each memory parameter can be modified individually, the easiest method of optimizing memory performance is to use memory that supports XMP profiles. These profiles are pre-programmed by the memory module manufacturer in the memory module itself and can be selected through the BIOS Memory Configuration. These profiles are validated by the manufacturer and are optimized for both performance and system stability.

The BIOS is designed to use the XMP memory profiles as a starting point for extending memory performance by more sophisticated system tweakers by then taking manual control after selecting an XMP profile.

In addition, memory population will also affect system performance. The Intel Core i7-800 and Intel Core i5-700 processor series have two independent memory channels. On Intel Desktop Board DP55KG/DP55SB, these are indicated by the two blue memory sockets. For best performance, populate the DIMM 0 of each channel, signified by the color blue with memory matched by manufacturer, size, speed, and type first. This matching allows the processor to access data across each of the memory channels by interleaving. Interleaving greatly reduces the latencies of waiting for memory to return data.

The two black memory connectors allow the end user to double the board's system memory capacity. Higher total memory capacity can have a positive benefit in very data-intensive applications. Always populate the blue memory sockets first for best system performance.

### 5.2. Memory Performance Options

The Memory Configuration page (Figure 10) contains all the memory timing options that can be adjusted and Table 2 describes the Performance Memory Profiles.

System Setup				
Performance				
Memory Configuration		Use pre-set memory latencies chosen by memory vendor.		
Performance Memory Profiles Uncore Multiplier Memory Multiplier tCL tRCD tRP tRASmin tRFC tRRD tWR tWTR tWTR tRTP tRC tFAW Memory Voltage Uncore Voltage Override Command Rate	<pre></pre>	Warning: Altering memory voltage may reduce system stability, cause the processor and other system components to fail, cause reductions in system performance, and/or affect system data integrity.		
		↑↓=Select Item Enter=Select Submenu F9=Setup Defaults F10=Save and Exit Esc=Previous Page		
	Changes			

Figure 10. Memory Configuration

Setup Option	Description				
_	Memory Performance Options				
<i>Note: All multiplier values listed below are applied to the single 133 MHz (default) host clock.</i>					
Performance Memory Profiles	Automatic setting uses default values provided by the memory module.				
	Manual mode allows the user to change each of the settings.				
	If the installed memory module supports <b>XMP</b> , there will be additional selections for each profile stored in the module. Selecting a profile will populate all the settings with those recommended by the manufacturer.				
Uncore Multiplier	Sets the Uncore CPU multiplier. This is non-adjustable for Intel Core i7 and Intel Core i5 processors. It is shown for information purposes.				
Memory Multiplier	Sets the memory multiplier provided by the on die memory controller of the installed processor.				
tCL	<b>CAS Latency</b> : The amount of time in cycles between sending a read command and the time to act on it.				
tRCD	<b>RAS to CAS Delay</b> : The amount of time in cycles for issuing an active command and the read/write commands.				
tRP	<b>RAS Precharge Time</b> : This is the minimum time between active commands and the read/writes of the next bank on the memory module.				
tRASmin	Minimum RAS Active Time: The amount of time between a row being activated by precharge and deactivated.				
tRFC	<b>RAS Refresh Cycle Timing</b> : This determines the amount of cycles to refresh a row on a memory bank.				
tRRD	<b>RAS to RAS Delay</b> : The amount of cycles that it takes to activate the next bank of memory.				
tWR	Write Recover Time: The amount of cycles that are required after a valid write operation and precharge.				
tWTR	Write to Read Delay: The amount of cycles required between a valid write command and the next read command.				
tRTP	<b>Read to Precharge Delay</b> : The minimum delay prior to when a Read can occur.				

### Table 2. Performance Memory Profiles

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Setup Option	Description				
Memory Performance Options					
Note: All multiplier values listed below are applied to the single 133 MHz (default) host clock.					
tRC	<b>Row Cycle Time</b> : The minimum time interval between successive ACTIVE commands to the same bank.				
tFAW	Four Active Window – Period of time before the fifth successive ACTIVE command to a new memory Bank can be issued.				
Memory Voltage	Changes the memory voltage.				
Uncore Voltage Override	Allows the CPU Uncore voltage to be adjusted. (A good starting Voltage for high frequency operation is setting the Uncore Voltage to 1.30 V).				
Command Rate	The amount of time that commands can be issued.				

Note: For each of the timings, except the multipliers, the lower the number, the higher the performance and the less stable the system may become.

### 5.3. Determining Memory Frequency

Over all, memory frequency can easily be determined by multiplying the host clock frequency (default is 133 MHz) by the Memory Multipliers shown in Table 3.

Multiplier	BCLK (MHz)	Resulting Memory Frequency (MHz)
6	133	800
8	133	1066
10	133	1333
12	133	1600

Table 3. Memory Multipliers

Increasing the host clock will increase the memory speed as well.

*Note: The Intel Core i7-800 and Intel Core i5-700 processor series support only even multipliers.* 

### 5.4. Using XMP Profiles

XMP memory profiles can be invoked by selecting them from the Memory Configuration page (Figure 11).

	System Setup	
Performance		
Memory Configuration	(T. C.I. 2, UNT 2422)	Use pre-set memory latencies chosen by memory vendor.
Performance Memory Profiles Uncore Multiplier Memory Multiplier tCL tRCD tRP tRASmin tRFC tRRD tWR tWTR tWTR tRTP tRC tFAW Memory Voltage Uncore Voltage Override Command Rate	<pre><profile 2:="" xmp-2133=""> [18] &lt;12: DDR3-2137&gt; [9] [9] [9] [26] [110] [7] [15] [8] [8] [8] [8] [45] [30] &lt;1.6500U&gt; &lt;1.3000U&gt; &lt;6uto&gt;</profile></pre>	Warning: Altering memory voltage may reduce system stability, cause the processor and other system components to fail, cause reductions in system performance, and/or affect system data integrity.
		†↓=Select Item Enter=Select Submenu F9=Setup Defaults F10=Save and Exit Esc=Previous Page
	Changes	

Figure 11. Selecting XMP Profiles

Note: It has been found during testing that earlier XMP memory modules that work correctly with the socket 1366 Intel Core i7-900 processor series have a higher (Uncore Voltage Override) voltage and do not perform as well at the higher voltage when used with the socket 1156 Intel Core i7-800 and Intel Core i5-700 processor series. It is recommended to manually adjust the Uncore voltage to a starting value of 1.35 V or lower if these memory modules are used (see Figure 12).

Note: It is very important to check the memory voltage (see Figure 12) after applying the profile to ensure that the DIMM has not selected a memory voltage above the recommended processor memory controller voltage limit. If it has, switch the profile setting to "Manual – User Defined" and reduce the voltage to avoid damage to the processor.

	Sustem Setur	
Performance		
Memory Configuration		
Performance Memory Profiles Uncore Multiplier Memory Multiplier tCL tRCD tRP tRASmin tRFC tRRD tWR tWTR tRTP tRC tFAW Memory Voltage Uncore Voltage Override Command Rate	<pre>(Manual - User Defined&gt; [18] (12:     1.3500V [9]     1.3400V [9]     1.3300V [26]     1.3200V [1110     1.3100U [7]     1.3000V [15]     1.2900V [8]     1.2700V [8]     1.2600V [45]     1.2600V [45]     1.2600V (1.6     v </pre>	<pre> fl=Select Item PgUp/PgDn=Scroll List Enter=Change Setting Esc=Discard Changes</pre>
	Changes	

Figure 12. Checking the Uncore Voltage Override

Once the profile has been applied, save the settings and boot into the operating system to verify system stability using the same methods described in Section 4.3.

### 5.5. XMP Manual Mode

XMP profiles can provide a starting point for stretching memory performance even further. To begin, choose the XMP profile first, presetting all profile values. At this point choose "Manual – User Defined" to set values further.

From here, increase the performance further by setting the tCL, tRCD, tRP, and tRAS values to their maximum settings and increase the host clock in small increments while checking system stability between each clock increase until the system becomes unstable.

Once the point of instability has been reached, back down the clock frequency and start reducing the tCL, tRCD, tRP, and tRAS settings until the system once again becomes unstable, then reduce them by one increment.

For XMP profiles above DDR3 1600, the BIOS will automatically raise the system BCLK to reach those memory speeds. This means that when using an Intel Core i7-870 processor running at a base speed of 2.93 GHz, the processor will run at a maximum frequency of 4.51 GHz. If this type of fast memory is used, be sure to look at the other processor parameters in the BIOS and ensure they are set to support the faster processor frequency.

### 6. Examples of Over-Clocked Settings

### 6.1. Using the Host Clock for Performance Boost

The example shown in Figure 13 uses host clock (BCLK) adjustments to reach higher system performance.

Note: That the higher processor frequency will only be reached as long as there is sufficient thermal margin to support the higher speeds and the installed processor can handle this frequency.

	System Setup	
Main Configuration Performance Sec	urity Power Boot Exit	
BIOS Version	KGIBX10J.86A.3822.2009.0928.1349	Number of cores to
Processor Type	Intel(R) Core(TM) CPU 870 @ 2.936Hz Intel® 64 Architecture Capable	processor package
Active Processor Cores	<all></all>	
Intel® Hyper-Threading Technology	<enable></enable>	
Overridden Host Clock Frequency	167 MHz	
Overridden Processor Turbo Speed	4.51 GHz	
Overridden Memory Speed	2004 MHz	
L2 Cache RAM L3 Cache RAM Total Memory Memory Channel A Slot 1 Memory Channel A Slot 0 Memory Channel B Slot 1 Memory Channel B Slot 0	256 KB 8192 KB 4096 MB Not Installed 2048 MB Not Installed 2048 MB	
System Identification Information		++=Select Screen
System Date System Time	[10/08/2009] [13:33:13]	↑J=Select Item FJ=Select Submenu F9=Setup Defaults F10=Save and Exit Esc=Previous Page

Figure 13. System Overclocked to 3.67 GHz from 2.93 GHz with Turbo Disabled

Figure 14 is a display of the Performance Tab showing Default (with Turbo enabled), Proposed, and Active adjustments.

	System	Setup		
Main Configuration <mark>Performance</mark>	Security Power	Boot Ex	kit	
Failsafe Watchdog Host Clock Frequency Override Host Clock Frequency (MHz)	<mark><enable></enable></mark> <manual> [167]</manual>			Mechanism to restore safe values if system cannot boot.
Host Clock Frequency	Default 133 MHz	Proposed 167 MHz	Active 167 MHz	
Processor Overrides				
Intel® Turbo Boost Technology Core Max Multiplier Sneed	Enable 27 3 60 GHz	Enable 27 4 51 GHz	Enable 27 4 51 GHz	
Memoru Querrides	J:00 0H2		1.31 002	
Multiplier Speed Uncore Multiplier Uncore Speed Bus Overrides	10 1333 MHz 18 2.40 GHz	10 1670 MHz 18 3.01 GHz	10 1670 MHz 18 3.01 GHz	
Processor Core Memory Processor Uncore PCH Core	Dynamic 1.5000V 1.1000V 1.0300V	Dynamic 1.5000V 1.1000V 1.0300V	Dynamic 1.5000V 1.1000V 1.0300V	←→=Select Screen ↑↓=Select Item Enter=Select Submenu F9=Setup Defaults F10=Save and Exit Esc=Previous Page

Figure 14. System Overclocked with Default Processor Overrides

Figure 15 is a display of the Performance Tab with Bus Overrides showing other board voltages at default. PCH Core Voltage might need to be adjusted when raising Uncore Voltage under the configuration page to achieve stable operation.

	System
Performance	
Bus Overrides	
PCH Core Voltage Override	<1.03000>
PCI Express Configuration	
PCI Express Bus Frequency	<default></default>
PCI configuration	
PCI Bus Frequency	33.33 <b>M</b> Hz

Figure 15. Bus Overrides at Default



	System Setup	
Performance		
Memory Configuration		Can be increased if overclocking. Warning:
Performance Memory Profiles	<manual -="" defined="" user=""></manual>	Altering memory
Uncore Multiplier	[18]	voltage may reduce
Memory Multiplier	<12:	system stability,
tCL	[9] 1.7000V	cause the processor
tRCD	[9] 1.6900V	and other system
tRP	[9] 1.6800V	components to fail,
tRASmin	[27] 1.6700V	cause reductions in
tRFC	[110 <u>1.6600</u>	system performance,
tRRD	[7] 1.6500V	and/or affect system
tWR	[19] 1.6400V	data integrity.
tWTR	[8] 1.6300V	
tRTP	[8] 1.6200V	
tRC	[45] 1.6100V	
tFAW	[30] 1.6000V	
Memory Voltage	<1.6	
Uncore Voltage Override	<1.3000V>	
Command Rate	<auto></auto>	
		↑↓=Select Item PgUp/PgDn=Scroll List Enter=Change Setting Esc=Discard Changes
	Changes	

Figure 16. Memory Configuration with Uncore Voltage Adjusted in Manual Mode

Figure 17 shows the Memory Performance increased with the BCLK using the maximum Memory Multiplier.

	System	) Setup		
Main Configuration <mark>Performance</mark> S	Security Power	· Boot Ex	it	
Failsafe Watchdog Host Clock Frequency Override Host Clock Frequency (MHz)	<mark><enable></enable></mark> <hanual> [192]</hanual>			Mechanism to restore safe values if system cannot boot.
Hest Clack Fragmancu	Default 133 MHz	Proposed	Active	
Processor Auerrides	111 11112	172 1112	172 1112	
Intel® Turbo Boost Technology Core Max Multiplier	Enable 27 3.60.6Hz	Disable 17 3 27 GHz	Disable 17 3 27 GHz	
Memoru Overrides	3:00 012	J-21 012	3.21 012	
Multiplier Speed Uncore Multiplier Uncore Speed	10 1333 MHz 18 2.40 GHz	12 2305 MHz 18 3.46 GHz	12 2305 MHz 18 3.46 GHz	
Bus Overrides				
Voltages Processor Core Memory Processor Uncore PCH Core	Dynamic 1.5000V 1.1000V 1.0300V	Dynamic 1.6500V 1.3000V 1.0300V	Dynamic 1.6500V 1.3000V 1.0300V	↔=Select Screen ↑↓=Select Item Enter=Select Submenu F9=Setup Defaults F10=Save and Exit Esc=Previous Page

Figure 17. Memory Performance Increased to 2305 MHz

Figure 18 shows what happens when you push the system too far either in Memory Performance or Processor Performance.

The system BIOS has detected unsuccessful POST attempt(s). Possible causes include recent changes to BIOS Performance options or recent hardware changes.
Press 'Y' to enter BIOS Setup or press 'N' to cancel and attempt to boot with previous settings.

Figure 18. Watchdog Timer Tripped