

TRADEMARK USAGE GUIDELINES for Licensees, Press and Other Third Parties

The trademarks of MIPS Technologies, Inc. are valuable and important intellectual property assets of our company. It is important that you also protect the value of our trademarks by using them properly. These Guidelines, which are updated from time to time, set out our policies for your use of the company's trademarks. Please, properly identify our trademarks as outlined below to accurately reference our products and services.

If you are a licensee of MIPS Technologies' trademark, your license agreement will specify the marks that you are authorized to use and may provide special trademark usage guidelines.

You may NOT use our logos and design trademarks or taglines without our prior written authorization, except as noted below for the MIPS-Verified[®] mark.

You may NOT use our trademarks in a manner that incorrectly suggests that MIPS Technologies sponsors or endorses or is otherwise associated with your activities, products, and services.

MIPS, MIPS I, MIPS II, MIPS III, MIPS IV, MIPS V, MIPS-3D, MIPS16e, MIPS32, MIPS64, microMIPS, MIPS-Based, MIPS Navigator, MIPS System Navigator, MIPSsim, MIPS Technologies Logo, MIPS-Verified, MIPS-Verified Logo 4K, 4Kc, 4Km, 4Kp, 4KE, 4KEc, 4KEm, 4KEp, 4KEpro, 4KS, 4KSd, M4K, M14K, M14Kc, 24K, 24Kc, 24Kf, 24KE, 24KEc, 24Kef, 34K, 34Kc, 34Kf, 74K, 74Kc, 74Kf, 1004K, 1004Kc, 1004Kf, 1074K, 1004Kc, 1004Kf, R3000, R4000, R5000, R10000, "At the core of the user experience.", BusBridge, CorExtend, CoreFPGA, CoreLV, EC, iFlowtrace, JALGO, Malta, MDMX, OCI, PDtrace, Pro Series, SEAD-3, SmartMIPS, SOC-it, and YAMON are trademarks or registered trademarks of MIPS Technologies, Inc. in the United States and other countries.

Basic Trademark Rules

Distinguish our marks. *ALWAYS visually set off trademarks from the surrounding text (which means, at a minimum, it must be capitalized).*

Accompany our marks. ALWAYS use the trademark as a proper adjective and follow the mark with an appropriate noun; i.e., the name of the service, good, or generic product type to which it refers (e.g., the "5Kc processor core"). Our Trademark List (see below) shows the company's trademarks and the goods and/or services to which they apply. The trademark symbol appearing with each mark indicates the registration status of that mark in the United States.

Marks are never a noun or a verb. A trademark should only be used as a proper adjective.

Marks are never plural or possessive.

Never modify the form of a mark, whether the trademark is an acronym, word, words or graphic design. Word trademarks should not be modified by abbreviations, translations or connections (*e.g.*, by a hyphen or otherwise) to other words or trademarks. All logos should be reproduced in strict compliance with the established graphical form and in the correct colors.

Notice our marks using a proper symbol. If you use our mark(s) on goods listed below in the U.S., use the symbol designated below. If you use our mark(s) exclusively outside of the U.S. and/or for goods not listed below, use a TM notice symbol.

Attribute our marks by properly acknowledging our ownership interest in them (e.g., "[Mark] is a trademark or registered trademark of MIPS Technologies, Inc. in the United States and other countries.") Such attribution statement may appear in any conventional location within a document or packaging (e.g., header, footer, footnote, etc.).

Special Rules--For Authorized Licensees of the MIPS-Verified Logo

General Rule: The Logo may only be used by authorized Licensees in association with products based on Release 2 of the MIPS32 and MIPS64 architectures that have been validated and authenticated by Licensees using the Verification Test Suites provided by MIPS Technologies.





When to use the MIPS-Verified Logo: The Logo can and should be used on all (i) product labels or on external packaging, (ii) in sales literature and other collateral material, (iii) in user manuals, product data sheets, and other technical documentation, and (iv) in advertising, communications and public relations materials for Licensee's products based on the following MIPS Technologies' products:

<u>Cores</u>: 4KE family, 4KSd, M4K, M14K family, 24K family, 34K family, 74K family, 1004K family, and 1074K family. <u>Architectures</u>: MIPS32 R2, MIPS64 R2, microMIPS, MIPS16e, DSP, MT, SmartMIPS and MIPS-3D.

Intact Logo, Size and Proportion:

The Logo must always be reproduced exactly as pictured above-in the specific typefaces shown, with the word "MIPS" always resting above the word "VERIFIED," and the design element positioned as indicated. No other typefaces are permitted. The Logo can be reproduced in color or black& white and may be proportionately enlarged or reduced so long as it appears no smaller than approximately 2 cm by 1 cm.

A Licensee may refer to a product in text as "Authenticated by the MIPS-Verified[™] test suites" or "Authenticated using MIPS-Verified[™] verification technology."

Usage with other logos: The Logo is an independent trademark and should not be incorporated into other marks, logos, and artwork. The Logo may appear in proximity to Licensee's marks and logos and other artwork, but with a clear visual separation.

Do NOT use the MIPS-Verified Logo with products based on Release 1 cores and architectures, such as:

<u>R1 Cores</u>: 20Kc, 25Kf, 4K family and 5K family. <u>R1 Architectures</u>: MIPS32 R1, MIPS64 R1, MIPS16, MDMX and MIPS I through V.

TRADEMARK	ABBREVIATED GOODS/SERVICES
4K TM	Core family, cores;*
4Kc TM	Hard cores; cores;*
4Km TM	Cores;*
4Kp TM	Cores;*
4KE TM	Core family, cores;*
4KEc TM	Hard cores; cores;*.
4KEm™	Cores;*
4KEp TM	Cores;*
4KEPro TM	Cores;*
4KS TM	Core family, cores;*
4KSd TM	Cores;*
24K TM	Core family, cores;*
24Kf TM	Cores;*
24Kc TM	Hard cores; cores;*
24KE TM	Core family, cores;*
24KEc TM	Cores;*
24KEf TM	Cores;*
34K TM	Core family, cores;*
34Kc TM	Cores;*
34Kf TM	Cores;*
74K TM	Cores;*
74Kc TM	Cores;*
74Kf TM	Cores;*
1004K TM	Cores;*
1004Kc TM	Cores:*
1004Kf tm	Cores;*
1074Ктм	Cores;*

TRADEMARK LIST

TRADEMARK	ABBREVIATED GOODS/SERVICES
1074Кстм	Cores:*
1074Кf ^{тм}	Cores:*
M4K TM	Cores:*
М14Ктм	Cores:*
М14Кстм	Cores:*
"At the core of the user experience."®	Microprocessor cores; Software; Documentation.
"At the core of the networked experience." "At the core of the home entertainment experience." "At the core of the portable media experience." "At the core of everything that keeps you connected to life."	Microprocessor cores; Software; Documentation.
BusBridge™	Core interface; modules.
cnMIPS®	Processors, microprocessors, and controllers; semiconductor cores for processors, microprocessors, and controllers; Documentation; Consulting services.
CorExtend®	Instructions, instruction extensions, user-defined instructions blocks, UDI blocks, modules, UDI modules.
CoreFPGA™	Daughter cards.
CoreLV TM	Daughter cards.
ЕСТМ	Interfaces.
FPGAView TM	Software.
iFlowtrace®	Microprocessor core; cores; software; debug technology; debug solution; debugger
Malta TM	Development boards; motherboard.
MDMX TM	Processor architecture, application specific extension to architecture ("ASE"); Software; Documentation; Services.
microMIPS®	Processor architecture, Software; Documentation; Services.
MIPS®	Microprocessors; Microprocessor cores; Processor architecture, instruction set architectures ("ISA"), application specific extension to architectures ("ASE"); Reduced instruction set computer-based hardware; Software; Documentation.
	Microprocessors; Microprocessor cores; Processor architecture, instruction set architectures ("ISA"), application specific extension to architectures ("ASE"); Hardware; Software; Documentation.
MIPSTM	Services (generally), including design services and education services.
MIPS TECHNOLOGIES (Logo)®	Microprocessors; Microprocessor cores; Processor architecture, instruction set architectures ("ISA"), application specific extension to architectures ("ASE"); Hardware; Software; Documentation.
TECHNOLOGIES MIPSI™	Processor architecture, instruction set architecture ("ISA").
MIPS II TM	Processor architecture, instruction set architecture ("ISA").
MIPS IIITM	Processor architecture, instruction set architecture ("ISA").
MIPS IVTM	Processor architecture, instruction set architecture ("ISA").
MIPS VTM	Processor architecture, instruction set architecture ("ISA").
MIPS16e®	Processor architecture, instruction set architecture ("ISA"), application specific extension to architecture ("ASE"); integrated circuits and microprocessors; Documentation.
MIPS-3D [®]	Processor architecture, application specific extension to architecture ("ASE"); Software; Documentation; Services.
MIPS32 [®]	Processor architecture, instruction set architecture ("ISA"); Microprocessor cores; Software; Documentation; Services.
MIPS64 [®]	Processor architecture, instruction set architecture ("ISA"); Microprocessor cores; Software; Documentation; Services.

TRADEMARK	ABBREVIATED GOODS/SERVICES
MIPS-Based TM	Semiconductors, integrated circuits, circuit chips, microprocessors and, printed circuit boards.; Brochures and pamphlets; information relating to microprocessors.
MIPS Navigator TM	Tools; Debugger; Debug Technology
MIPS System Navigator™	Tools; Debugger; Debug Technology
MIPSsim™	Simulators, simulation systems, software.
MIPS-Verified®	Test suites; Verification technology; Trade secret-protected verification technology.
MIPS-Verified (Logo)® MIPS: Verified	Test suites; Verification technology; Trade secret-protected verification technology.
OCITM TM	Debug logic; debug block; on-chip instrumentation
PDtrace TM	Interfaces; tracing logic; specifications.
Pro Series TM	Printed circuit boards, development boards, semiconductors, integrated circuits, microprocessors; microprocessor cores; documentation; services; core family.
R3000®	Information about microprocessors.
R3000 ^{тм}	Microprocessors
R4000 [®]	Information about microprocessors.
R4000 ^{тм}	Microprocessors.
R5000®	Information about microprocessors.
R5000 ^{тм}	Microprocessors.
R10000 ^{тм}	Microprocessors.
SEAD-3™	Development boards.
SmartMIPS [®]	Processor architecture, instruction set architecture ("ISA"), application specific extension to architecture ("ASE"); Software; Documentation.
SOC-it [®]	Platform, Integrated circuits, microprocessor-based systems, controllers, controller cores, system controllers, system controller cores, and computer code; Software; Documentation.
System Navigator™	Tools; Debugger; Debug Technology
YAMON TM	Software; electronic documentation.

* microprocessor cores; processor core; software; documentation; processor; microprocessor, soft core, synthesizable core. microarchitecture; technology; design.

TRADEMARK USAGE GUIDELINES FOR LICENSEES, PRESS, AND OTHER THIRD PARTIES (Revision 3.3; November 7, 2011)