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PARTI

In this series of articles we hope to shed useful light on the high-frequency performace of amplifiers. Modern operational amplifiers and circuits of similar topology have an inherent Slew Rate (SR) limit, and they will produce distortion as the output Signal Slope (SS) approaches this limit. We refer to this distortion as Slew Induced Distortion (SID). If an amplifier is driven into slew-rate limiting gross distortion will be produced. This

is analogus to driving an amplifier into amplitude clipping, which also produces gross distortion. The distortion produced by driving an amplifier towards slew-rate limiting has also been described as Transient Intermodulation distortion (TIM) [3,8,9,17,18,51,56].

Until recently [33,34] there has not been a thorough study of this distortion. Therefore, this series is intended to be a comprehensive overview and explanation of SID. We will explain how and when SID is produced by an amplifier, and measurement techniques for and typical measurements of this distortion will be described. The results of a listening test for SID will be discussed, and the results of a theoretical calculation of SID in a 741 op amp will be shown and compared with measurements. Some reasonable design criteria will also be reviewed. Above all, we will attempt to give a good overall perspective of this subject so that the reader will be able to judge its relevance to his or her own situation.

Before discussing how SID occurs within amplifiers, it is necessary and appropriate to first consider how the slew rate itself is related to an audio signal. A sine-wave audio signal has definite and measurable parameters, namely its amplitude and frequency. However, a somewhat more subtle parameter (and one germane to this issue) is the *slope* of the signal, as is determined by its amplitude and frequency. A simple relationship which defines the signal slope (SS) of a sine wave is the equation

$$SS = 2\pi V p f \tag{1}$$

where Vp is the peak signal voltage, and f its frequency.



Sometimes this equation may be seen written in terms of slew rate (SR)[20,21,22,29,30,54], however we wish to clarify the point here that signals in themselves have no inherent slew limit, or maximum allowable slope, as do amplifiers. Therefore, we will use the terminology of SS to describe the slope of a sinewave (or other) signal and SR to describe the slew rate of an amplifier. Note that this is an important distinction, as an

(2)

amplifier has a defined SR, which is (by very definition) its maximum output-voltage rate of change, or slope, as set by its design. It is a defining performance limit for that amplifier, just as power output is (or any other basic performance parameter, for that matter).

The reader should note that this equation may be manipulated into an expression in terms of a frequency (f), for a given signal slope and peak voltage; for instance:

f=

When the relation is thus used, and the particular SS under discussion is the slew rate limit of a given amplifier and Vop its peak output voltage, it would appear as

$$fp = SR/2\pi Vop.$$
(3)

This expression yields a power bandwidth, fp, which is determined by the amplifier SR and the peak output voltage, Vop. Generally, fp is understood to be the bandwidth for a 1 percent THD limit. Note that fp is directly proportional to SR and inversely proportional to Vop. The practical significance of this is that high output-voltage amplifiers require more SR to maintain a given distortionless bandwidth.

Also, an important distinction to be made is that power bandwidth defines an entirely different form of bandwidth than does the more familiar small-signal bandwidth, and the two terms should never be confused. Exceeding the power bandwidth of an amplifier causes gross distortion; exceeding its small-signal bandwidth results only in a frequency response rolloff [37].

SID and TIM

Which is Which and What Do They Mean?

Unfortunately, many of the popular explanations serve to confuse rather than clarify the issue, and this short preparatory discussion will, we hope, clarify some of these points to the reader.

"TIM" stands, of course, for *transient intermodulation distortion*, sometimes called simply "transient distortion." If this name is taken in a literal sense, it implies a distortion

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Fig. 1 — Mixed square/sine output from amplifiers with and without TIM. General conditions: 5-kHz square wave and 40-kHz sine wave. Fig. 1a — Strong TIM, sine wave missing on waveform transitions, slewing evident. (Scale: 10 V/div.) Fig. 1b — Little or no TIM, waveform is a linear sum of sine and square waves. (Scale: 1 V/div.)



Fig. 1a

Fig. 1b



Fig. 2a

Fig. 2b

Fig. 2 — Amplifier square-wave responses with and without slew limiting. Fig. 2a — Slew limiting (10 kHz, 10 V p-p). Fig. 2b — No slew limiting (10 kHz, 1 V p-p).

mechanism which produces intermodulation when subjected to transients. A point to be noted is that if the term were understood literally, this would imply transients of *both high and low* frequencies *and/or* high or low operating levels. In other words, *all* transients.

In actual practice, however, transient IM occurs only for signals with simultaneous high level and high frequencies not lower levels or lower frequencies. The key parameter of such signals is that they are characterized by *high signal slopes*, not just high frequencies or high levels. Neither high frequencies nor high levels in themselves *necessarily* result in distortion, unless their combination is such that a high effective SS is produced.

High SS waveforms are not confined solely to transient waveforms. It just so happens that musical signals which exhibit high signal slopes more often are transient in nature —

Fig. 3 — Inter-relationship of amplifier response, feedback, and SID. Fc is the small signal bandwidth which varies for different gains. Fp is amplifier fullpower bandwidth which is independent of gain (for a given output level).



a fortissimo cymbal clash, for instance. Thus, TIM is probably a descriptive term for the distortion as it occurs on *musical* waveforms, but the term is not totally descriptive of the distortion mechanism itself [33,34,44,52].

TIM is actually generated when the SS approaches or exceeds the amplifier SR. Thus, a more easily understood term as to what actually happens would be one which relates *both* to SS and SR. In an amplifier, distortion is produced when the output voltage SS approaches or attempts to exceed the SR, as the amplifier limits (clips) for such a circumstance. This can happen for either transient or steady-state signals [33, 34, 52] if they have a sufficiently high SS. Thus we feel a more descriptive term to describe the mechanism is Slew Induced Distortion [33, 58] as it is distortion induced either by the onset of or actual slewing. Other descriptive variations of this terminology are seen in print, such as "slew rate distortion" and "slewing distortion," and mean essentially the same thing [11].

Effect of Excessive Signal Slope On Amplifier Performance

A demonstration of the sensitivity of amplifiers to SS is contained in the two waveform photos of Fig. 1. Figure 1a shows a mixed square/sine wave signal combination, where the level and risetime of the square wave are such that the SS is greater than the amplifier SR. For this particular output voltage, then, slew limiting is produced on the square-wave edges, causing the momentary disappearance of the sine wave. Note in particular the square wave transition in the center of the screen. This is, of course, a strong case of TIM, which is induced by the condition of slewing.

In 1b, the same signal is shown at a reduced level, and, as can be noted, the slew limiting is gone, as the waveform indicates simply a *linear* sum of the sine and square wave. The point being made here is that the distortion is not being caused so much by the transient as it is by the high SS (in Fig. 1a). Thus, it should be appreciated (in a qualitative sense) that SID (or TIM) is a distortion which is *level sensitive* in terms of both amplitude and frequency (since both affect SS).

This factor is demonstrated in another way by the squarewave response photos of Fig. 2. In Fig. 2a, a 10V p-p square wave is shown, and, as can be noted, the amplifier is slewing, as evident by the linear rising and falling waveform edges. In 2b, the waveform is at a lower level, and here the square wave is reproduced without slew limiting. This is evident by the *exponential* shape of the waveform edges, which is an indicator that the amplifier is operating linearly [15,36,37]. It is in actuality operating as a low-pass filter, as is defined by its small signal bandwidth, fc.

A square wave passed through a single-pole filter will exhibit the general waveform shape of Fig. 2b, and such a waveform at the output of an amplifier is a qualitative indicator that no slew limiting is present. At progressively higher voltage-output levels, slew limiting may set in (as in 2a), and the waveform then takes on the ramp-like slopes [15,37,50,63].

This is incidentally an excellent check to make on an amplifier if possible, increasing output square waves. If the exponential waveshape holds true for increases in level up to the rated output, the amplifier is behaving *optimally*, as it cannot be made to slew for any realistic signal conditions [11,43]. For this to be true, the power bandwidth must be greater than the small-signal bandwidth [45] which in turn says that the amplifier is guaranteed free from internal overload due to excessive SS. An amplifier can be designed for a defined small-signal bandwidth either by use of an input low-pass filter or appropriate feedback connections to constrain output SS below the SR. Further details of this from a Fig. 4 — Relative relationship of fc and fp, and the resulting effect on SID. General conditions: 5 kHz square wave, 20 V p-p.



design standpoint are contained in several references [11,43,45] and are also discussed later on.

The Effect of Feedback on SID

One of the popular explanations for the cause of TIM and SID is said to be excessive negative feedback used around audio amplifiers [3,4,6,7,8,9,10,13,48]. In fact, this appears to be one of the more volatile parts of the issue, even to the extreme that already there have appeared statements in the literature calling for maximum feedback factors on the order of 12 dB and amplifiers advertised as having "zero feedback." The general argument advanced is that increasing negative feedback increases the susceptibility to TIM, and optimum feedback factors are said to be on the order of 30 to 40 dB.

It is interesting to consider how changes in feedback will affect the performance of an amplifier. There are certain aspects of the "less feedback is better" school of thought which have definite merit, but the *entire* situation must be considered for a true and complete perspective.

Consider a fixed gain-bandwidth amplifier open-loop response, as illustrated in Fig. 3. This amplifier has a unitygain frequency of 1 MHz (such as a 741) and a full-power bandwidth of 10 kHz (at full output). Suppose we examine its susceptibility to SID for gains of 20, 40, and 60 dB, and at full output level. The small signal bandwidth (fc) for these three conditions will be 100 kHz, 10 kHz and 1 kHz, respectively [30]. However, for each condition of feedback, the fullpower frequency (fp) remains at 10 kHz. Then, for the 20-dB (heavy feedback) gain condition SID is definitely possible, for output frequencies of 10 to 100 kHz. For 40 dB of gain, fc is equal to fp, and slight SID is possible. For 60 dB of gain, fc is less than fp, so SID is not possible.

A demonstration of this is contained in the photos of Fig. 4, taken from an IC op amp operating fairly close to the conditions of Fig. 3. For this device fp is 17 kHz, and Fig. 4a shows a square wave for the condition where fc is greater than fp; slewing is evident. In 4b, fc is equal to fp, and some slewing is noticeable at the *initial* rise of the square wave where SS is highest. In 4c, fc is less than fp and no slewing is evident. In all three instances, the experiment follows what the Bode diagram predicts.

The reason that slewing is not evident for the high-gain, low-feedback condition is because the amplifier output SS is severely curtailed, due to the very low small-signal bandwidth. This is another demonstration of the point made above that slewing can be prevented by making fc less than fp. For a fixed gain-bandwidth amplifier, as just demonstrated, this generally says that less feedback can prevent or reduce susceptibility to TIM or SID, as it reduces fc in relation to fp, or lowers the output SS in relation to amplifier SR. This is however hardly the *optimum* manner to arrive at this objective, as it will most certainly result in a generally noisier and more distorted amplifier, as well as possibly insufficient bandwidth. If fc is to be maintained less than fp, it should be done by another method, obviously.

Another view on the "less feedback is better" argument is to consider an amplifier which is compensated (optimally)

for a higher gain (less feedback) condition. Due to fundamental feedback stability criteria, such an amplifier will have proportionally *less* compensation capacitance necessary. The smaller capacitance for less feedback then allows a higher SR to be realized by the amplifier, and so it is less susceptible to TIM or SID, as it can now handle greater SS waveforms linearly. In this case, the improvement is an indirect result of less feedback, a point which should be appreciated fully — *it also* results because the SR is raised.

These points are somewhat subtle, and we do appreciate that a fair amount of semantics are involved in the discussion which accompanies this issue. There are, however, several key points which are clear and should be made.

Since the limited SR is the cause of the distortion, it follows that design means which improve amplifier SR will lower distortion as a general result. (While this is generally true, there are notable exceptions, such as slew enhanced devices, which will be discussed later.) Feedback is certainly involved in the overall issue, but intimations that there is a fixed magical upper limit to feedback factors have no sound engineering basis to our knowledge. Given sufficient SR (and an otherwise linear amplifier), there is no inherent reason why 60 to 80 dB of feedback is not allowable [33,45,47,52]. The ultimate stability limit will, in practice, confine it to less than this as a natural consequence of usable gain-bandwidths, at least at audio frequencies.

Another part of the semantics issue comes to play with the argument that less feedback in combination with a more linear open-loop characteristic is desirable towards prevention of TIM. Essentially this is true, because without a high degree of overall feedback, less compensation (if any) is needed, and SR goes up as a result. However, *local* feedback around a stage *is still feedback*, and if bipolar transistors are used, it hardly seems possible to get truly excellent open-loop linearity without a lot of feedback, since their voltage transfer is basically exponential. So the argument should perhaps be oriented towards a closer definition of *what kind of feedback*, as well as its degree.

To get back to the more conventional amplifier, the point has been made that it is SR which is the fundamental predictor of SID (and/or TIM), and amplifier improvements which increase SR generally lower SID (and TIM).

The remaining low TIM criteria, wide open-loop amplifier bandwidth, involves semantics also. Taken literally, an openloop bandwidth of 20 kHz (as commonly specified) [1,2,3,4,6,7,8,10,14] will be interpreted to mean 20 kHz small signal bandwidth. What is really important is a 20 kHz (or more) power bandwidth, which will minimize or eliminate slew limiting [33,34,39,45,52].

Amplifiers can be designed for 20 kHz (or more) open-loop bandwidths, but often with a severe penalty of low-frequency linearity and gain accuracy [40,45]. By results from several different forms of tests, there appears to be no fundamental necessity for a wide open-loop small-signal bandwidth, given a power bandwidth sufficient to eliminate slew limiting. Several specific test results discussed later on clearly demonstrate this point.



Fig. 5b — Amplifier frequency response.



Analysis of

The Slew-Induced Distortion Mechanism

It is of fundamental importance to understand the various distortion sources in amplifiers, such as the SID mechanism of interest here. In this discussion we will mostly deal with operational amplifier circuits, but since many present-day power amps are of similar topology and are subject to similar physical laws, the discussion and data will be relevant to them as well.

Figures 5a is an idealized model of a typical operational amplifier [20,21,22,24]. Its input stage is a voltage-to-current converter or transconductance stage, characterized by the parameter g_m . The output current of this stage (Δi) is simply

$$\Delta \mathbf{i}_{(t)} = \mathbf{g}_{m} \Delta \mathbf{V}_{(t)}. \tag{4}$$

The second stage of the amplifier is an integrator, with an output voltage (Vo)

$$Vo_{(t)} = \underbrace{g_m}_C \int \Delta V_{(t)} dt.$$
 (5)

The resistor R is responsible for the finite d.c. gain of the amplifier. At low frequencies the open-loop gain is

$$Ao_{\pm}g_mR.$$
 (6)

The open-loop frequency response begins dropping (Fig. 5b) at a frequency

$$\omega_0 \neq 1/RC.$$
 (7)

Since for audio circuits we have no great interest in the amplifier gain at d.c., it is much more convenient to neglect R (as in equation 5) and work with the unity gain bandwidth

(ω u) which, due to the integrator's -6 dB/octave response, is equal to the gain bandwidth product.

$$\omega u = A_{(\omega)} x \omega$$
(8)
= $A_o \omega_o = g_m/c.$

Referring to equation 5, we have

$$o_{(t)} = \omega u \int \Delta V_{(t)} dt.$$
 (9)

Thus, for an amplifier with a six dB/octave frequency response, the amplifier can be characterized simply by its unity-gain bandwidth or gain-bandwidth product. Our next step is to examine the differential input voltage as a function of the output voltage. Differentiating equation 9 we have

$$\Delta V_{(t)} = \frac{1}{\omega_{\rm U}} \quad \frac{\mathrm{d} V o_{(t)}}{\mathrm{d} t}.$$
 (10)

This highly important result clearly shows us that the instantaneous differential input voltage of an amplifier is directly proportional to the slope of the output voltage, with $1/\omega u$ as the constant of proportionality.

If we now look at an actual amplifier, we will understand what SID really is. Figure 6a is a very simple real amplifier which will serve to demonstrate this. Q1 and Q2 are the differential input pair, and Q3-Q4 form a current mirror. This Q1-Q4 stage is our transconductance amplifier with a trans conductance of

$$g_m = I_k / 2V_T \tag{11}$$

where $V_T = K_T/q$ (26 mV at room temperature). Q5, with its current source load I_A, forms our integrator, in concert with C. We will neglect the finite d.c. gain produced by R,





Fig. 7 — Amplifier with feedback.



inasmuch as it has no bearing on ωu (see above). Ideally the g_m stage output current (Δi) is

$$\Delta \mathbf{i}_{(t)=g_m} \Delta V_{(t)=1_k} (\Delta V_{(t)}) / 2 V_{\tau}.$$
⁽¹²⁾

However, this is only true when ΔV is small. The exact transfer expression for this input stage is [23].

$$\Delta i_{(t)} = I_k \tanh \left(\Delta V_{(t)} / 2 V_T \right). \tag{13}$$

As this expression shows, the transconductance stage is linear only for small signals, and thus will produce distortion for high output currents, when ΔV is large. Equations 12 and 13 are plotted in Fig. 6b and illustrate this point more clearly.

The maximum output current (limit) from our input stage is I_k . This determines the maximum rate of change of V_o , which is the slew rate of our amplifier. This is simply $SR = I_c/C$ (14)

 $SS_{output}/SR = \Delta i/l_k.$ (15) This relation is one important and useful, as will be seen. The ratio SS/SR we will here define as the *slew rate ratio* (SR ratio), which relates the output SS to the amplifier SR.

This ratio is easily measurable from outside the amplifier with a differentiator,

 $\Delta i/I_k = (1/SR)(dV_o/dt).$ (16)

Figure 6b graphically tells us that operating with a SR ratio >0.25 (or Δi >0.25I_k) will produce some obvious distortion. This is equivalent to saying that operation at greater than 25 percent of the amplifier's SR will produce distortion. This distortion depends solely on the SS of the output, hence our use of the term "Slew Induced Distortion." The amplifier is

producing distortion by being forced towards its SR limit; the distortion is slew induced.

So far we have been talking only of the amplifier with no mention of feedback and we have been discussing the openloop performance. Amplifiers are rarely used open loop, so we must turn our attention to the effects of feedback on amplifier performance. An important point to keep in mind as we discuss feedback is that feedback networks are placed around an amplifier and have no direct effect on its *internal* performance. Feedback alone will not effect the validity of any of the equations developed above. It will, however, under certain signal conditions, cause these relationships to be taxed, creating a SID-producing situation. This statement will become more clear with subsequent discussions (if not already so from the preliminary discussion).

As is well known, feedback reduces distortion. Let's take a qualitative look at how this happens. A simple feedback network has been placed around our amplifier in Fig. 7. The differential input voltage is

$$\Delta V = (V_{10}R_2 + V_0R_1) / (R_1 + R_2). \tag{17}$$

This is the error voltage which we would like to be zero, but it will be non-zero if V_o contains a gain or phase error, or distortion. If we operate the amplifier near its slew limit, we know that the amplifier transfer characteristic is very nonlinear (see 6b). The feedback will reduce this non-linearity from V_{in} to V_{out} , but it will necessarily still exist from ΔV to Vout. If the feedback is doing its job and producing a relatively clean signal at V_{out} , then it follows that the signal ΔV must be distorted. The distortion of ΔV must be of the proper magnitude and phase to compensate for the amplifier's internal nonlinearity, if it is in reality reducing distortion. A qualitative insight of this is contained in the waveforms shown in Fig. 8. These are pictures of the performance of a 748 op amp, compensated to unity gain by 30 pF and operated as shown in Fig. 7. The amplifier had the following performance (measured before the experiment):

5 V/div.

0.1 V/div.



$$SR = +0.97, -0.91 V/ \mu S.$$



Fig. 8 — A 748 op-amp operating under various conditions detailed in the table.

The amplifier was operated at its full rated output swing of 20 V p-p. Two test frequencies were used, 12.7 kHz and 19.1 kHz. At 20V p-p (10V peak) these frequencies produced signal slopes of ± 0.8 V/ μ S and ± 1.2 V/ μ S respectively. These two frequencies were applied to the closed-loop amplifier, for signal gains of 1 and 10. For either gain condition, the output was a visibly clean sine wave for the 12.7 kHz, ± 0.8 V/ μ S signal (not shown). However, the 19.1 kHz, ± 1.2 V/ μ S signal drove the amplifier into slew limiting, and this is shown in Fig. 8b. The output slewing waveform was visibly the same for either gain. Table I summarizes and identifies the conditions and results shown.

The important point to note from this is that the op-amp input, ΔV , becomes highly distorted in an attempt to linearize the response of the closed-loop amplifier. In 8a and 8d, for example, ΔV is just beginning to become non-linear, but is still relatively low in level. As the maximum slew rate is exceeded, this process breaks down and the error voltage abruptly increases, as can be noted in 8c and 8e (note the different scale factors for ΔV). Operation at the lower gains (more feedback) yields lower distortion operation, and allows low-distortion operation closer to the slew rate limit.

There is nothing particularly unique about SID in audio amplifiers. It can be measured, calculated, and improved upon by using standard techniques that have been available for some time [57]. The only elusive aspect of this form of distortion is that rather than occurring on a peak magnitude (like clipping), it occurs on the rising or falling edge of the waveform, when the SS approaches or exceeds the amplifier SR. This is due to the fact that the dominant non-linearity in the circuit, the transconductance of the input stage, is followed by an integrating stage. Thus in Fig. 5, if the transconductance stage were overloaded and producing clipped square waves of current output, the integrating stage would transform these square waves into triangle waves at the output. The triangle wave is the ultimate example of gross slewing distortion, and its presence is a visible verification that the amplifier is operating open loop during the slew interval(s).

Although slew limiting is most often encountered in amplifiers due to *internal* IC relations, such as have been just described, it can also occur due to output-current/load-capacitance rate limiting, with the end effect being similar [33,34]. This type of slew limiting can occur for example in RIAAequalized preamps which cannot adequately charge frequency-shaping capacitors [33,41] or power amplifiers which cannot drive capacitive loads due to protection circuitry [33].

The distortion products produced by SID are measurable either by methods of THD [16], two-tone high-frequency IM, or TIM [14,33,34,51], and in all cases they become significant as the amplifier's inherent SR is approached by the output signal slope.

Representative results from these test methods are discussed in Part II of this series. In this next installment, sample data from different types of distortion tests are presented consisting of total harmonic distortion (THD), two-tone-difference intermodulation distortion (IM), and the recently proposed test for TIM [18]. Some of the relative merits of these measurement techniques will be discussed, and it will be seen that while they are all useful to the detection of this distortion, there are differences in sensitivity and practicality between them. Generally speaking, low-frequency distortion tests such as 1-kHz THD, or 60-Hz/7-kHz (SMPTE) IM tests are useless for detecting SID, since the signal slope is not sufficiently high. An interesting outcome is that IC op amps, long viewed with suspicion by many, are actually capable of truly superlative performance when properly operated below their slew-rate (SR) limit.

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