

IPMI Firmware / BIOS Release Notes Form

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Product Name	X10SRH-CF/CLN4F
Release Version	3.1
Release Date	6/6/2018
Previous Version	3.0a
Update Category	Critical
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.2. Changed BIOS version to 3.1.3. Added support for IPMI IPV6.4. Removed unsupported memory frequency options from setup menu.5. Updated Broadwell-EP RC 4.4.0 release.
New features	N/A
Fixes	<ol style="list-style-type: none">1. Fixed problem of Afu /O command clearing SMC SMBIOS region (\$SMC).2. Fixed problem of SUM OOB GetSataInfo always showing "Configuration Type" as "AHCI" when setting "Configure SATA as" to

	<p>"RAID" or "IDE".</p> <p>3. Fixed failure of SMBIOS to change to default even if preserve SMBIOS setup item is set to disabled during recovery.</p> <p>4. Fixed problem of the system debug card hanging at 79h after setting JBR1 to pins 2-3 (BIOS RECOVERY).</p> <p>5. Set Descriptor Region of BIOS Region Write Access to "No".</p>
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Release Notes from Previous Release(s) – these will be appended at the bottom of the current release notes in italic and in smaller print

3.0a (02/08/2018)

1. Updated Haswell-EP C0,1/M0,1/R2 CPU microcode M6F306F2_0000003C and Broadwell-EP B0/M0/R0 CPU microcode MEF406F1_0B00002A to address 'Spectre' variant 2 (CVE 2017-5715) security issue.
2. Updated BIOS version to 3.0a.
3. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v4.7.0.1014.
4. Updated Intel Server Platform Services 3.1.3.50 for Grantley Refresh Platforms.
5. Removed support for using Ctrl+home to trigger recovery.
6. Changed Memory correctable threshold to 100 and enabled Cloaking for Broadwell CPU E5-26xx SKU.
7. Updated BIOS ACM 3.1.1 PW and SINIT ACM 3.1.1 PW.
8. Added BBS reset function to SmcOobLoadBiosDefault().
9. Changed tCCD_L Relaxation default to 1 and changed string from Enabled to Auto.
10. Appended VPD - VB tag in VPD-W Tag(0x91) area.
11. Updated Broadwell-EP RC 4.3.0 PLR11 release.
12. Added SumBbsSupportFlag into DAT file.
13. Updated new MRC error log definition.
14. Enabled OA1_SUPPORT to fix inability to modify OEM Data by AMIBCP.exe.
15. Added Intel SPI vulnerability patch for Grantley.
16. Added "SMBIOS Preservation" Enabled/Disabled item for flash recovery.
17. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability.
18. Implemented SMC Recovery Flash Boot Block feature.
19. Fixed problem of system resetting or hanging after Watch Dog function is enabled during BIOS update.
20. Fixed inability to get correct Memory CECC DIMM location via SD5.